1 Cover Sheet
2 System Block Diagram
3 Intel LGA775 CPU - Signals
4 Intel LGA775 CPU - Power
5 Intel LGA775 CPU - GND
6 nVidia C55 - CPU / PCI-E
7 nVidia C55 - HT Link / Power
8 nVidia C55 - Memory A0
9 nVidia C55 - Memory A1
10 nVidia C55 - Gnd
11 DDR II DIMM 1 & 2
12 DDR II DIMM 3 & 4
13 DDR II Termination / EMI
14 nVidia BR04 - C55 Side
15 nVidia BR04 - PCIE x16 Slot
16 nVidia BR04 - Power / Gnd
17 nVidia MCP55 - HT / PCI / LPC
18 nVidia MCP55 - PCI-E
19 nVidia MCP55 - SATA / IDE / RGMII
20 nVidia MCP55 - Audio / USB / GPIO
21 nVidia MCP55 - PWR & GND
22 NB PCI-E x16 Pri / Sec Slot
23 SB PCI-E x16 Pri / Sec Slot
24 SB PCI-E x1 / PCI Slot
25 Gigabit LAN-MARVELL/88E8056
26 1394 Controller - JMB381
27 Azalia Codec - ALC888S
28 JMicron JMB363
29 Super I/O - ITE/IT8718F
30 Fan Controller
31 uPI ACPI Solution
32 uPI Power Regulator
33 VRD11- ISL6322 4 Phase 2 Channel
34 Front / Rear USB Connectors
35 ATX / Front Panel / LED
36 Audio De-Pop Circuit
37 Manual Parts
38 Power On/Off Sequence
39 Syatem Power Map
40 GPIO & Jumper Setting
41 System Reset Map
42 Project History

MS-7510 Version : 2.0 acer Beetle

CPU :

- Intel Wolfield Family and Yorkfield Family Processor
- Intel Conroe Family and Kentsfield Family Processor
- Intel Pentium D Processor 900 and 800 Sequence
- Intel Pentium 4 Processor 600 and 500 Sequence

System Chipset :

- nVidia C72XE [C55 + BR04]
- nVidia MCP55P

On Board Chipset :

- Azalia Codec -- RealTek ALC888S
- GB LAN 1 -- MARVELL/88E8056
- GB LAN 2 -- MARVELL/88E8056
- VRM 11 -- Intersil ISL6322
- ACPI Controller -- uPI Solution
- IEEE 1394a Controller -- JMicron JMB381
- eSATA Controller -- JMicron JMB363
- Super I/O -- ITE/IT8718F
- SPI Flash 8Mb

Main Memory :

- 2 Channel DDR II * 4 (Max 8GB)

Expansion Slot :

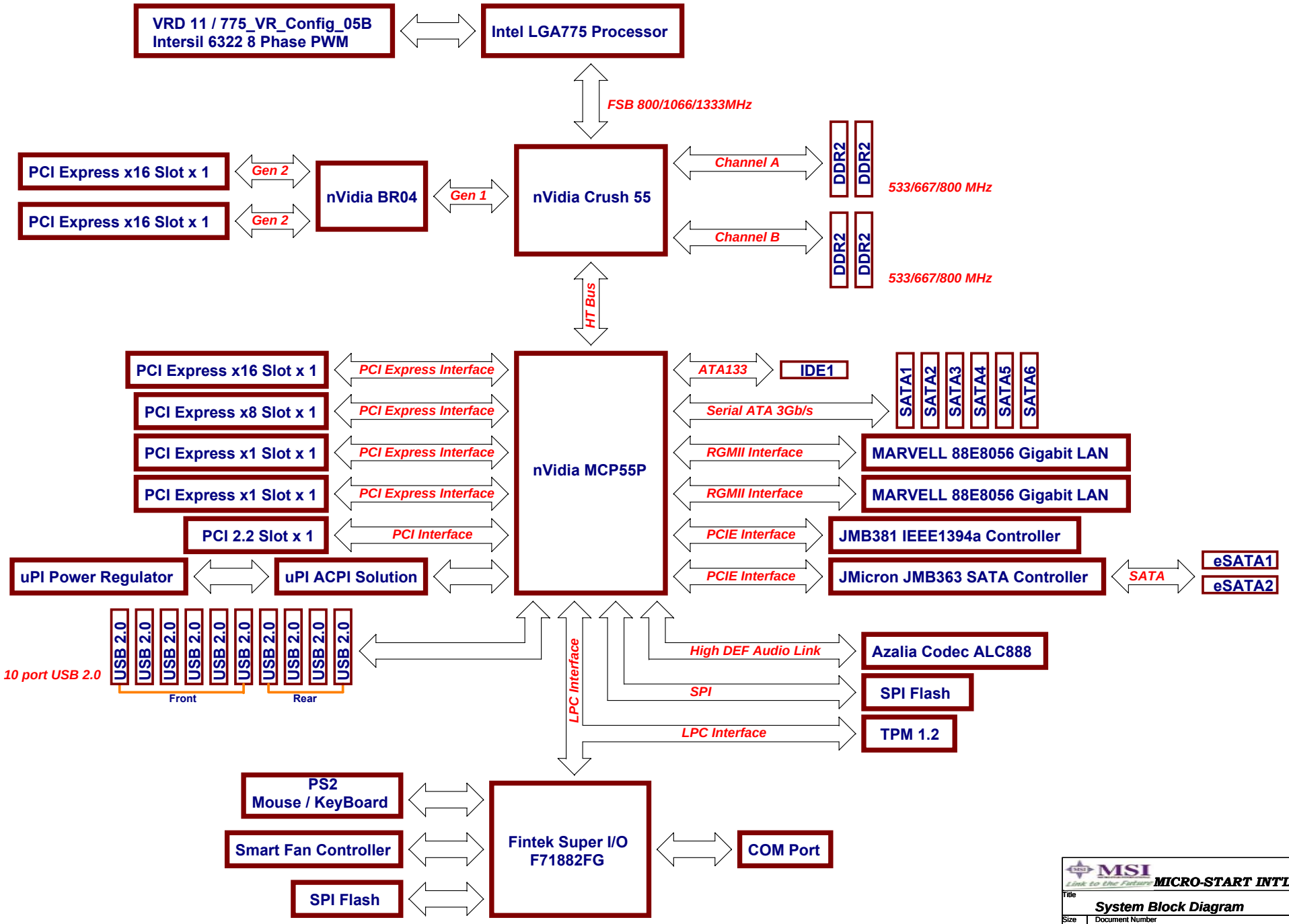
- PCI Express x16 Slot * 3
- PCI Express x8 Slot * 1
- PCI Express x1 Slot * 2
- PCI Slot * 1

ERP No.	Config Item	PlatForm or Option	Option Select
	Cfg-STD	C55 + BR04 + MCP55P + ALC888S + MARVELL/88E8056 + JMB381 + JMB363 + ITE/IT8718F	STD

MICRO-START INT'L CO.,LTD.

Title: Cover Sheet		
Size	Document Number: MS-7510	Rev: 2.0
Date: Thursday, January 10, 2008	Sheet 1 of 42	

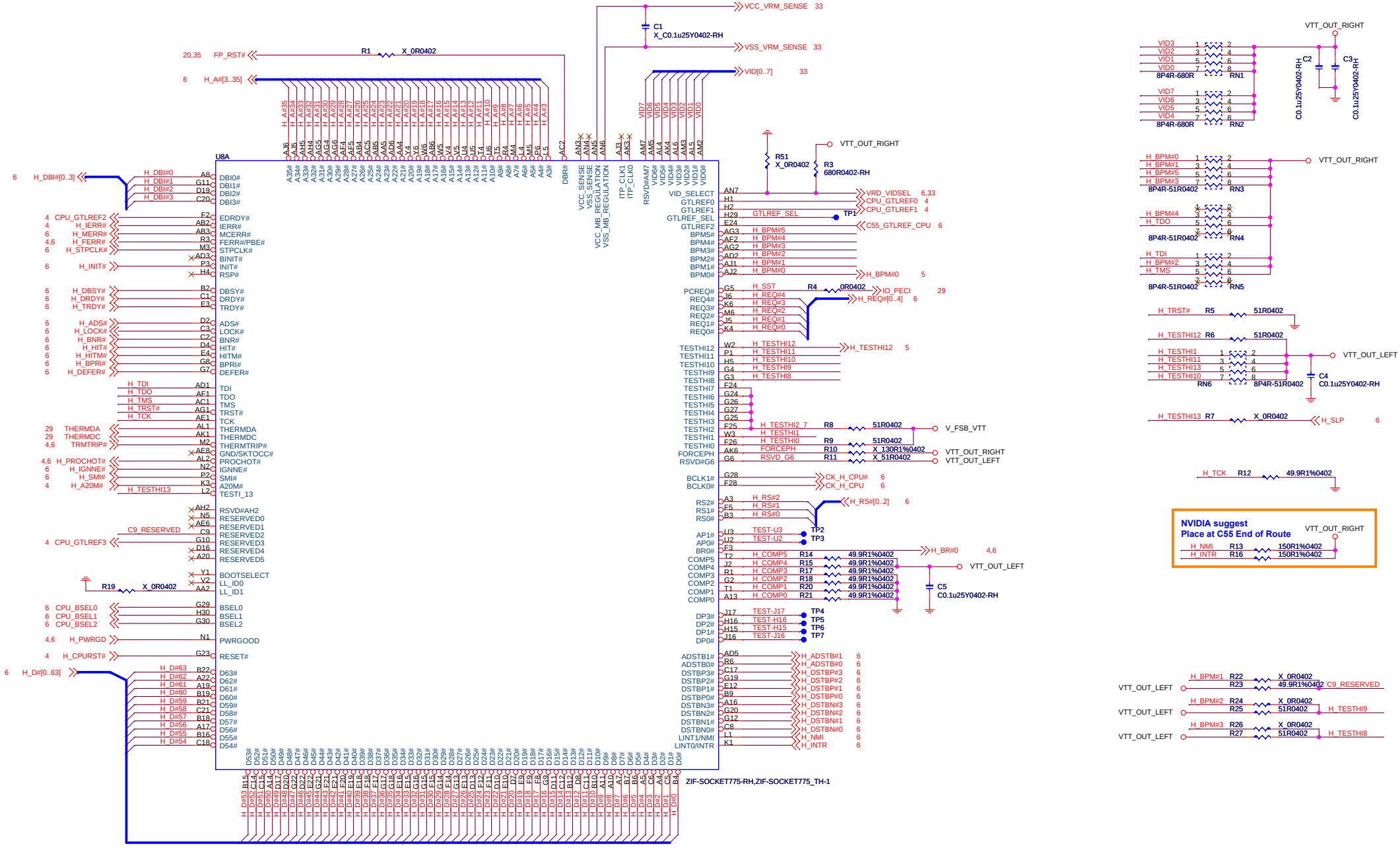
System Block Diagram



MICRO-START INT'L CO.,LTD.

Title: System Block Diagram		
Size	Document Number: MS-7510	Rev: 2.0
Date: Friday, January 11, 2008	Sheet 2 of 42	

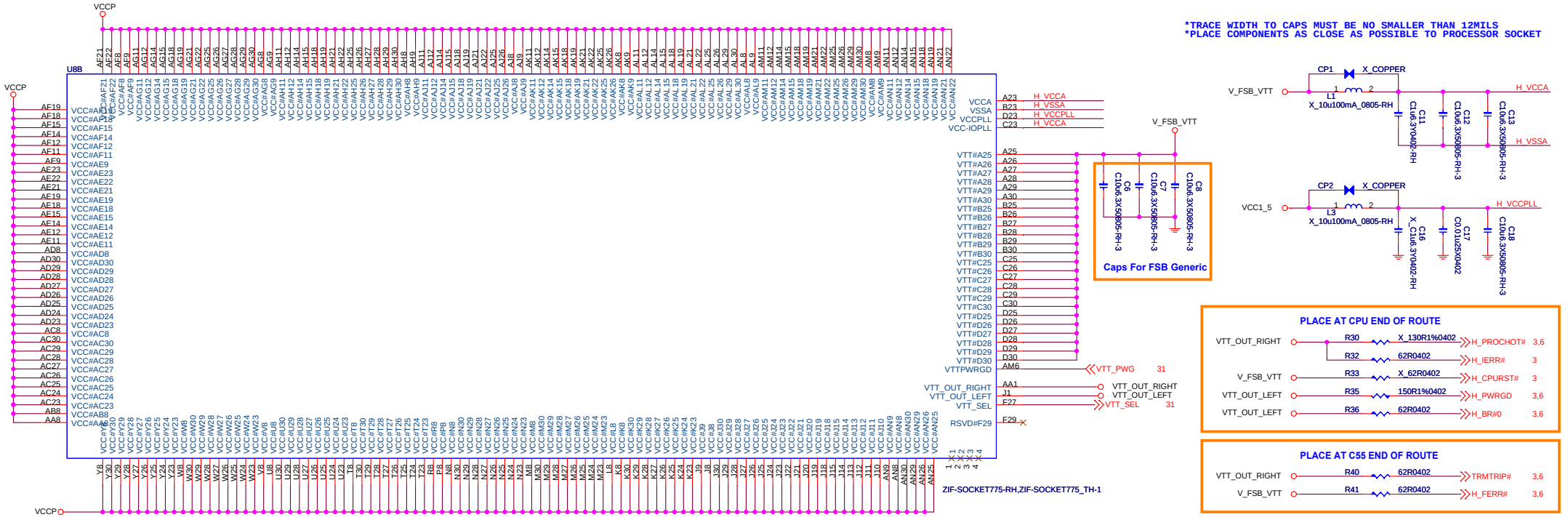
CPU LGA775 - Signals

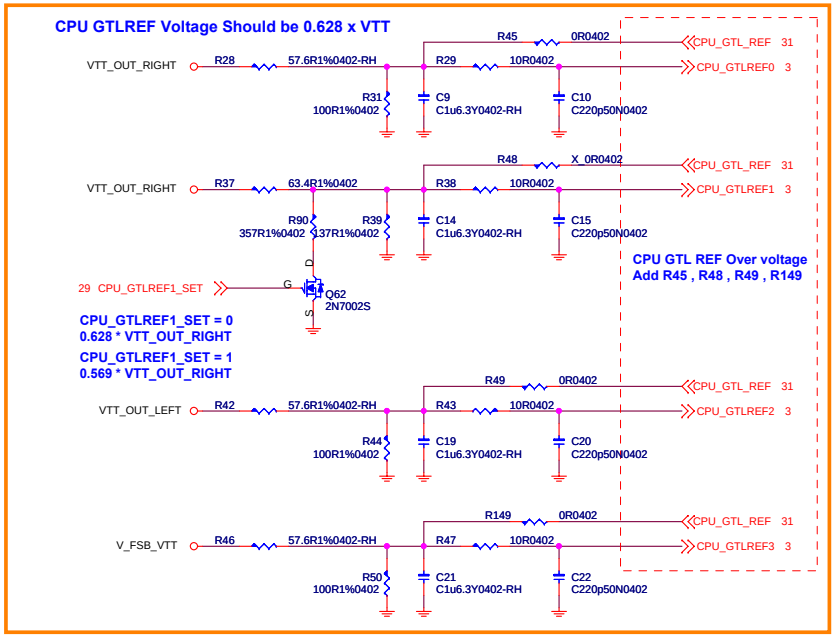


MICRO-START INT'L CO.,LTD.

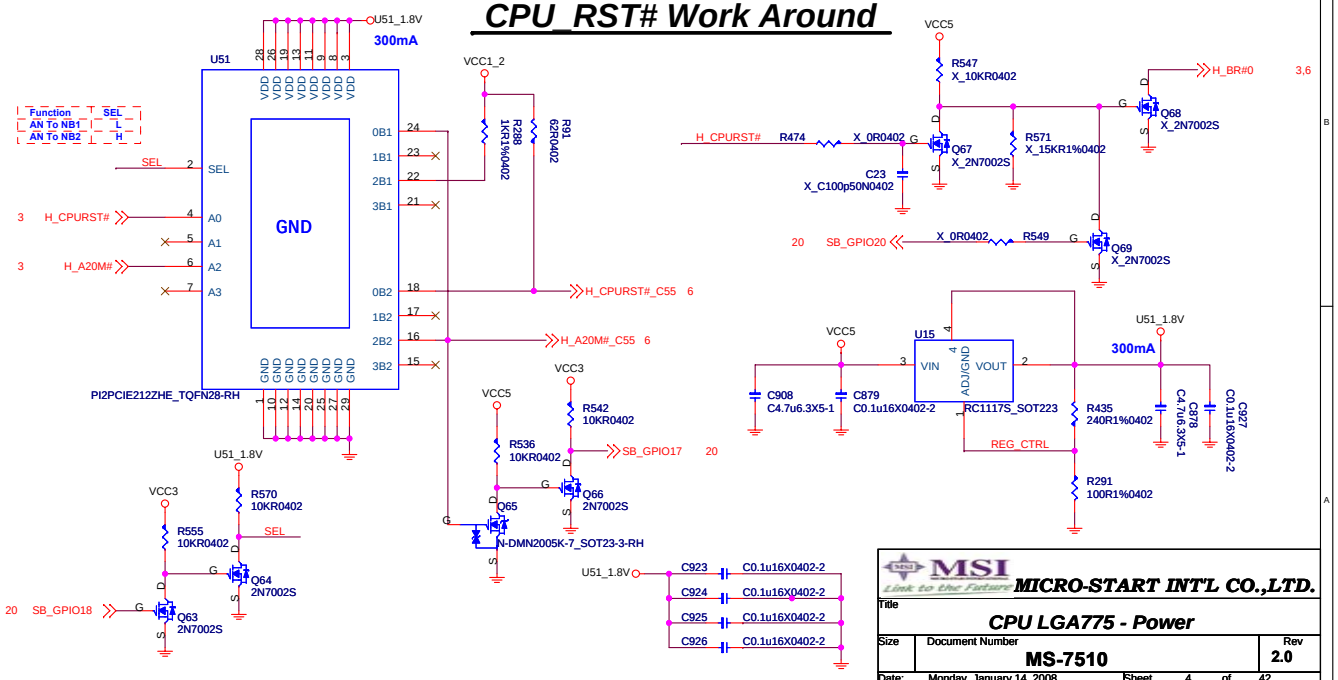
Title	CPU LGA775 - Signals	
Size	Document Number MS-7510	Rev 2.0
Date: Saturday, January 12, 2008	Sheet 3 of 42	

CPU LGA775 - Power



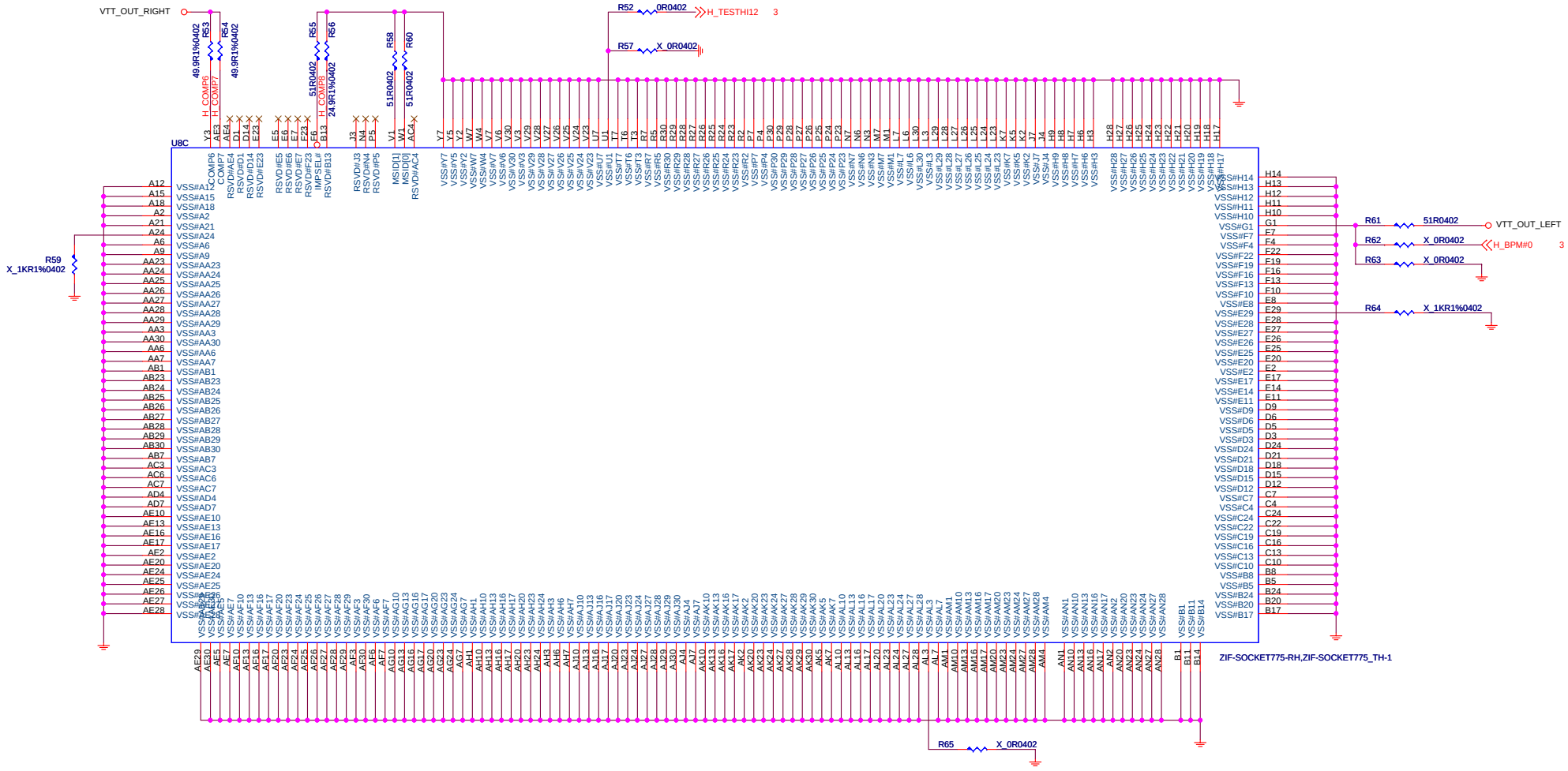


CPU_RST# Work Around



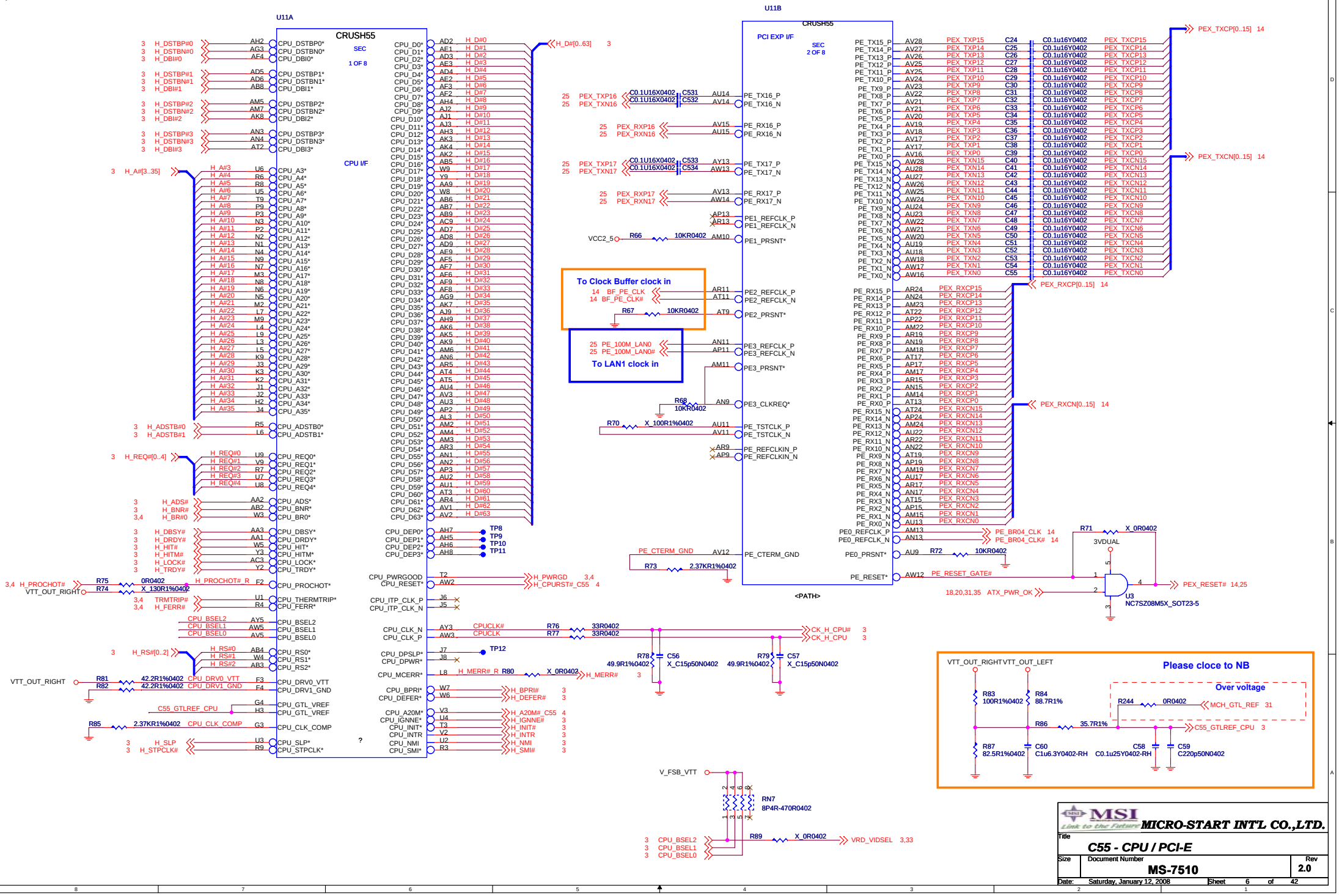
MSI Link to the Future	MICRO-START INT'L CO.,LTD.		
Title	CPU LGA775 - Power		
Size	Document Number	MS-7510	Rev 2.0
Date: Monday, January 14, 2008	Sheet 4 of 42		

CPU LGA775 - Gnd

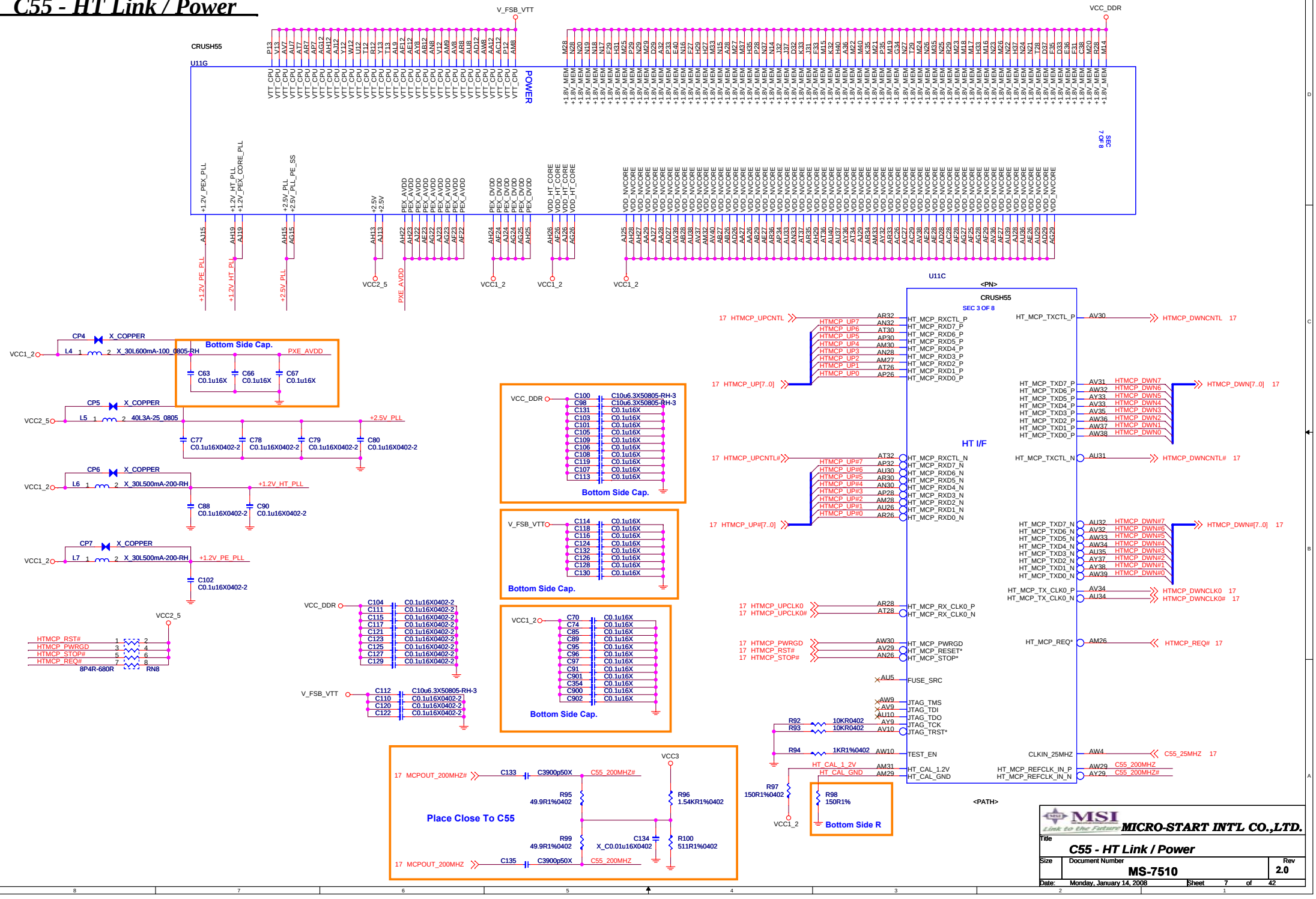


MSI Link to the Future	MICRO-START INT'L CO.,LTD.		
Title	CPU LGA775 - Gnd		
Size	Document Number	MS-7510	Rev 2.0
Date: Saturday, January 12, 2008	Sheet 5 of 42		

C55 - CPU / PCI-E

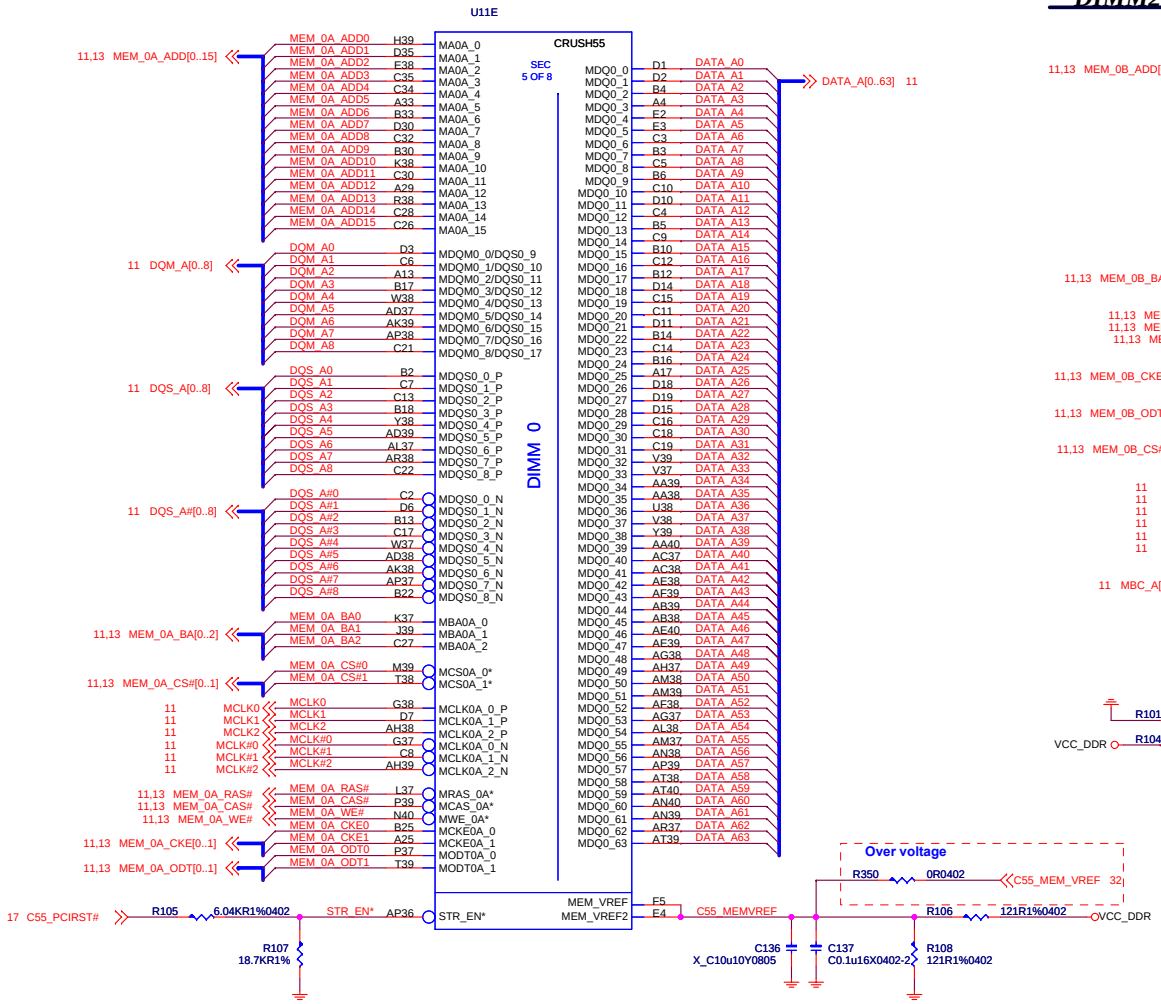

U11A
CRUSH55
SEC
1 OF 8
CPU I/F
U11B
CRUSH55
PCI EXP I/F
SEC
2 OF 8
To Clock Buffer clock in
To LAN1 clock in
<PATH>
Please cloce to NB
Over voltage
MICRO-START INT'L CO.,LTD.
C55 - CPU / PCI-E
MS-7510
2.0
Saturday, January 12, 2008

C55 - HT Link / Power

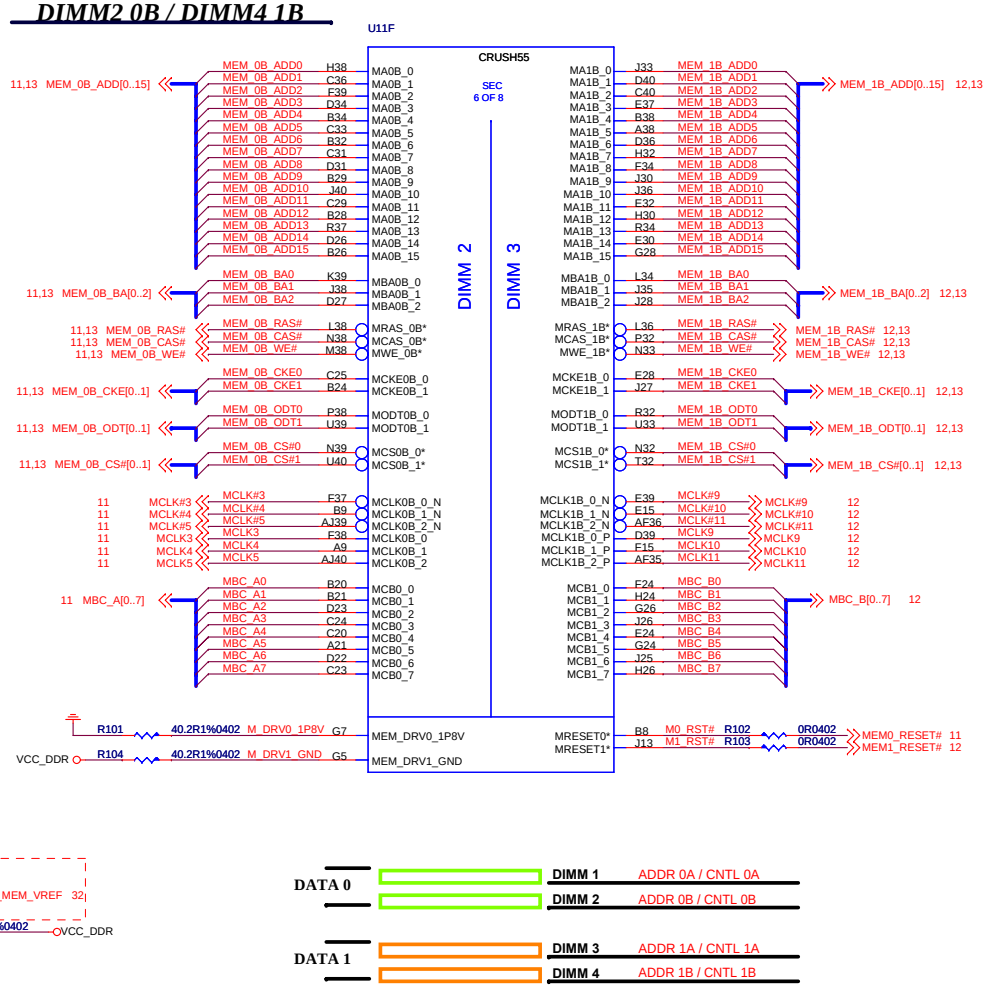


C55 - Memory A0

DIMM1 0A



DIMM2 0B / DIMM4 1B

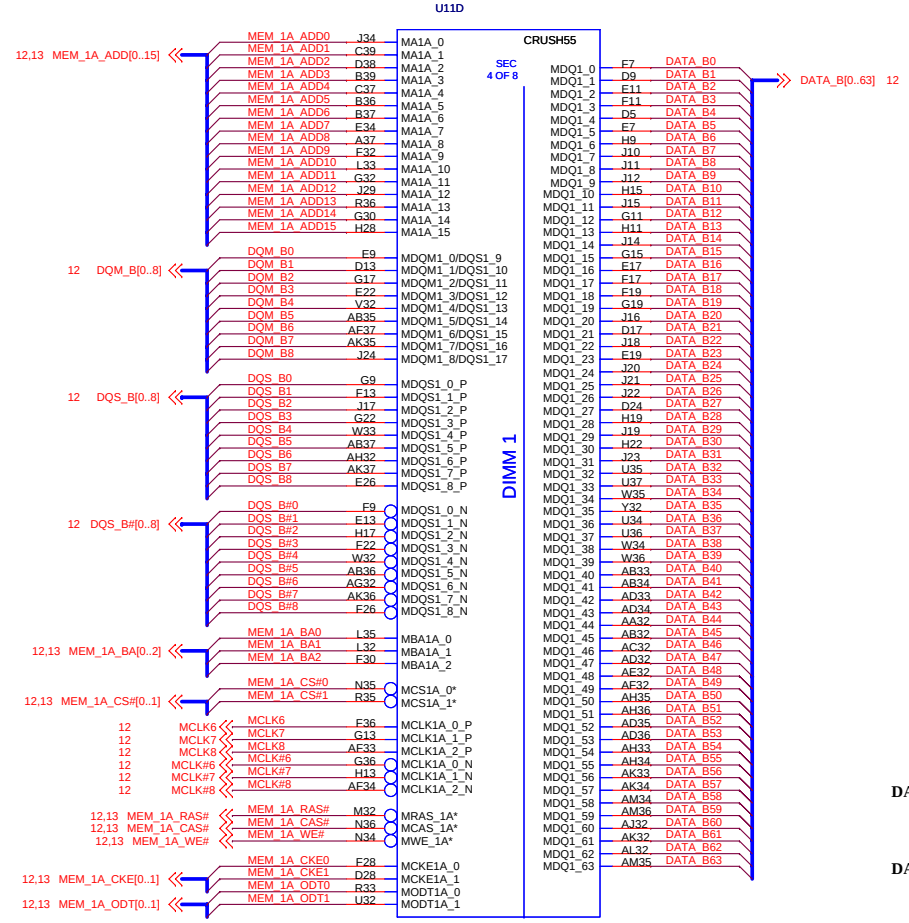


MICRO-START INTL CO.,LTD.

Title	C55 - Memory A0	
Size	Document Number MS-7510	Rev 2.0
Date: Saturday, January 12, 2008	Sheet 8 of 42	

C55 - Memory A1

DIMM3 1A



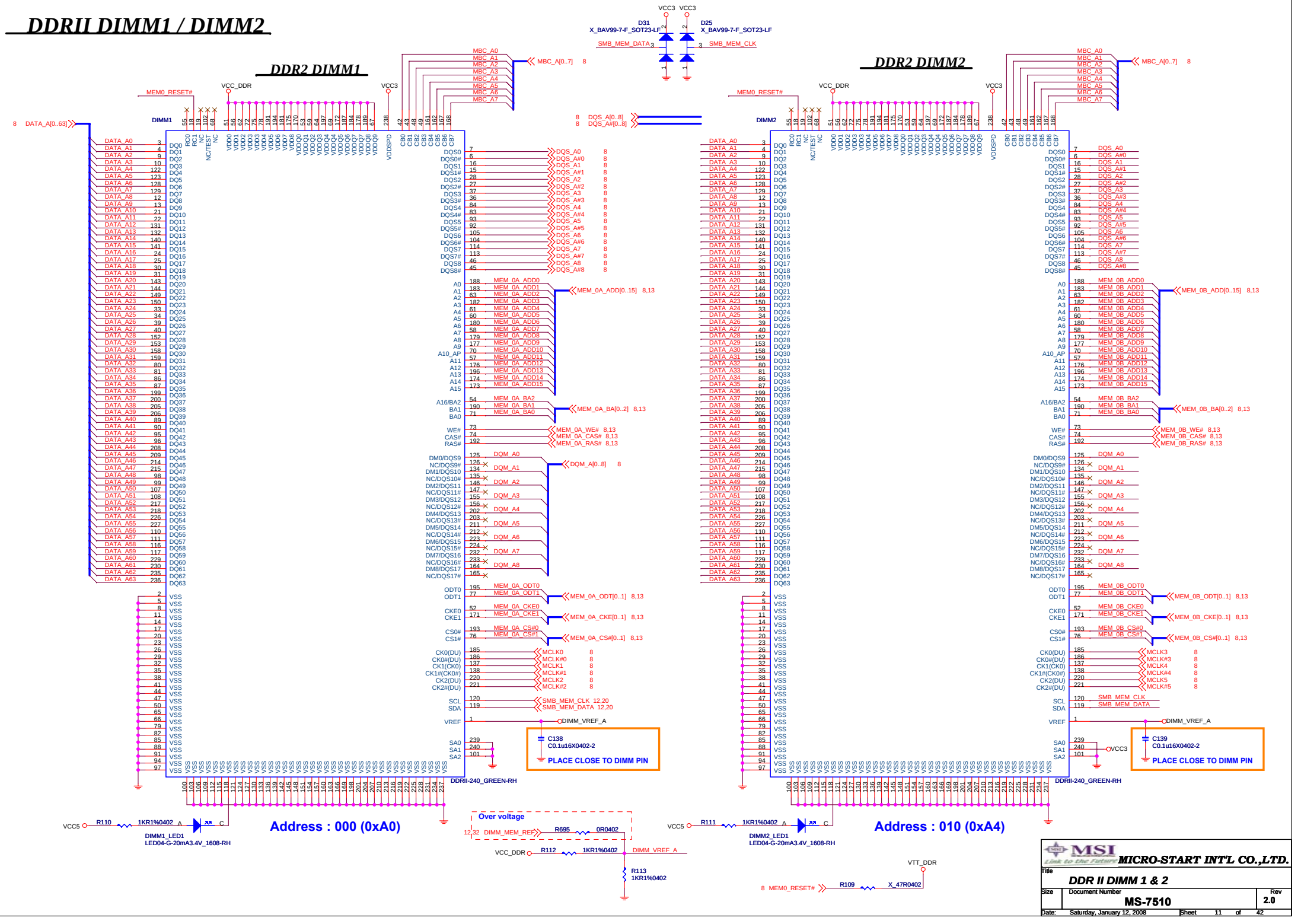
MICRO-START INT'L CO.,LTD.

Title	C55 - Memory A1	
Size	Document Number MS-7510	Rev 2.0
Date: Saturday, January 12, 2008	Sheet 9 of 42	

C55 - Gnd

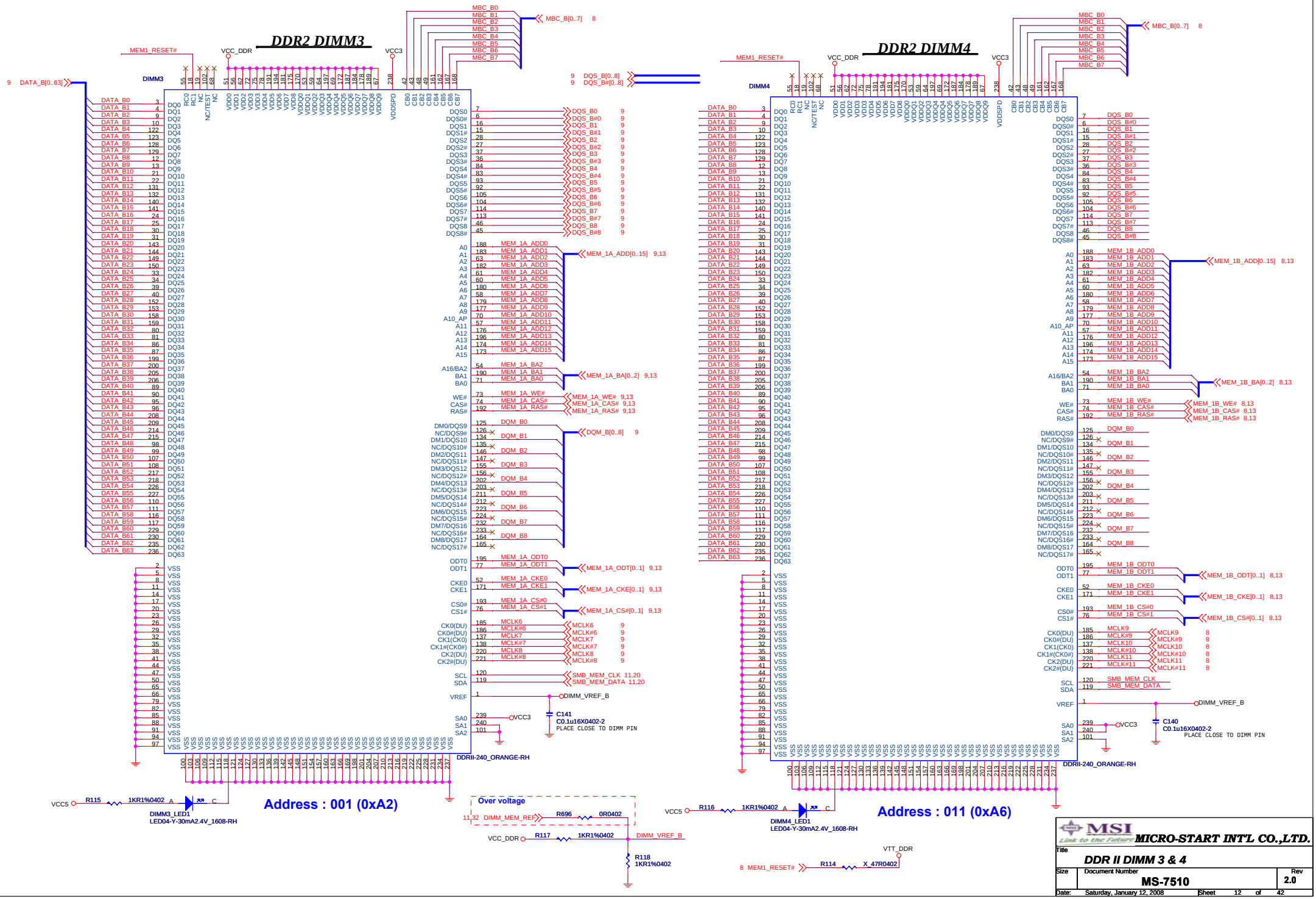
MSI Link to the Future	MICRO-START INT'L CO.,LTD.		
Title	C55 - Gnd		
Size	Document Number	MS-7510	Rev 2.0
Date: Friday, January 04, 2008	Sheet 10 of 42		

DDRII DIMM1 / DIMM2



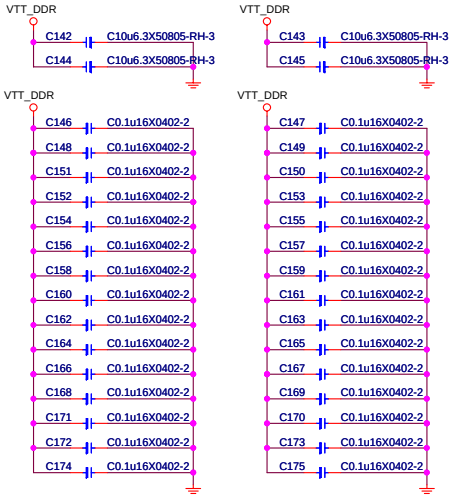
MICRO-START INTL CO.,LTD.			
Title: DDR II DIMM 1 & 2			
Size	Document Number: MS-7510		Rev: 2.0
Date: Saturday, January 12, 2008		Sheet 11 of 42	

DDRII DIMM3 / DIMM4

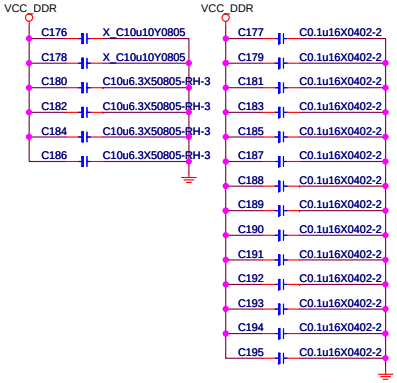


MSI Link to the Future	MICRO-START INTL CO.,LTD.		
Title	DDR II DIMM 3 & 4		
Size	Document Number	MS-7510	Rev 2.0
Date: Saturday, January 12, 2008	Sheet 12 of 42		

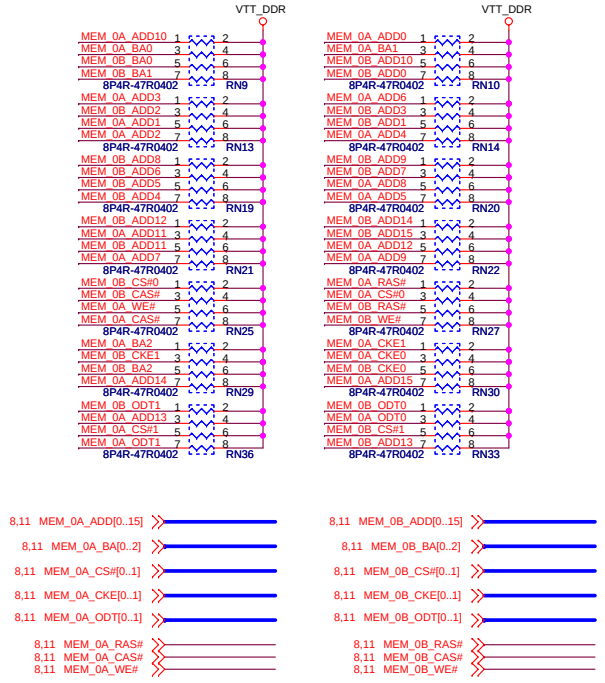
DDR Channel A VTT_DDR Decoulping Caps.



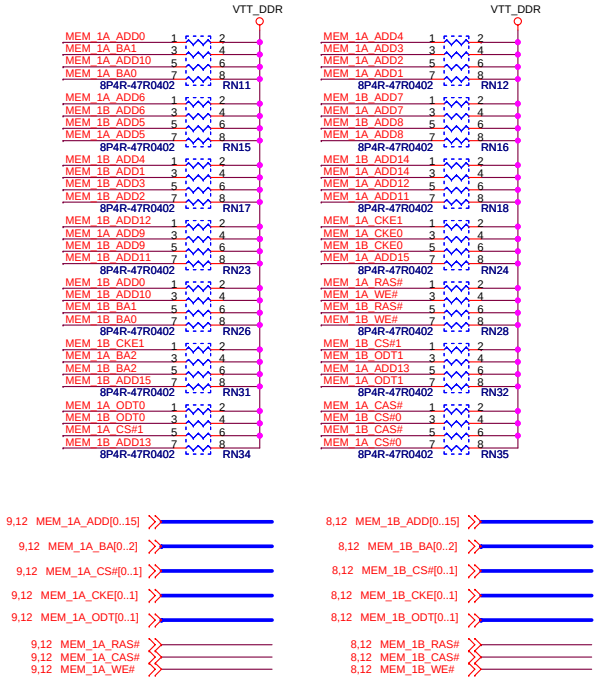
DDR Channel A VCC_DDR Decoulping Caps.



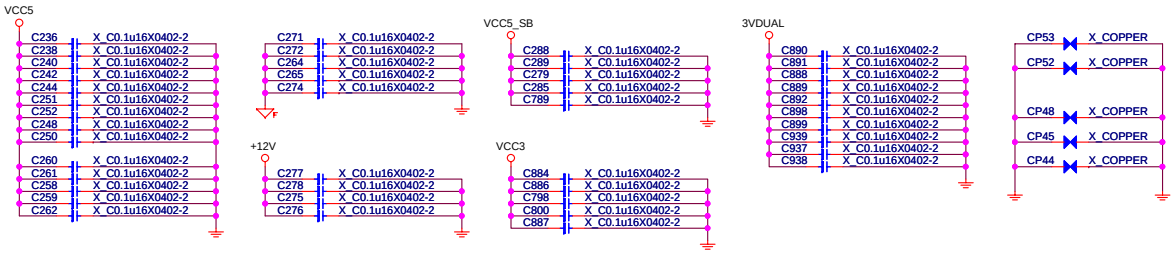
DDR Channel A Termination



DDR Channel B Termination



EMI Solution 2007-10-25



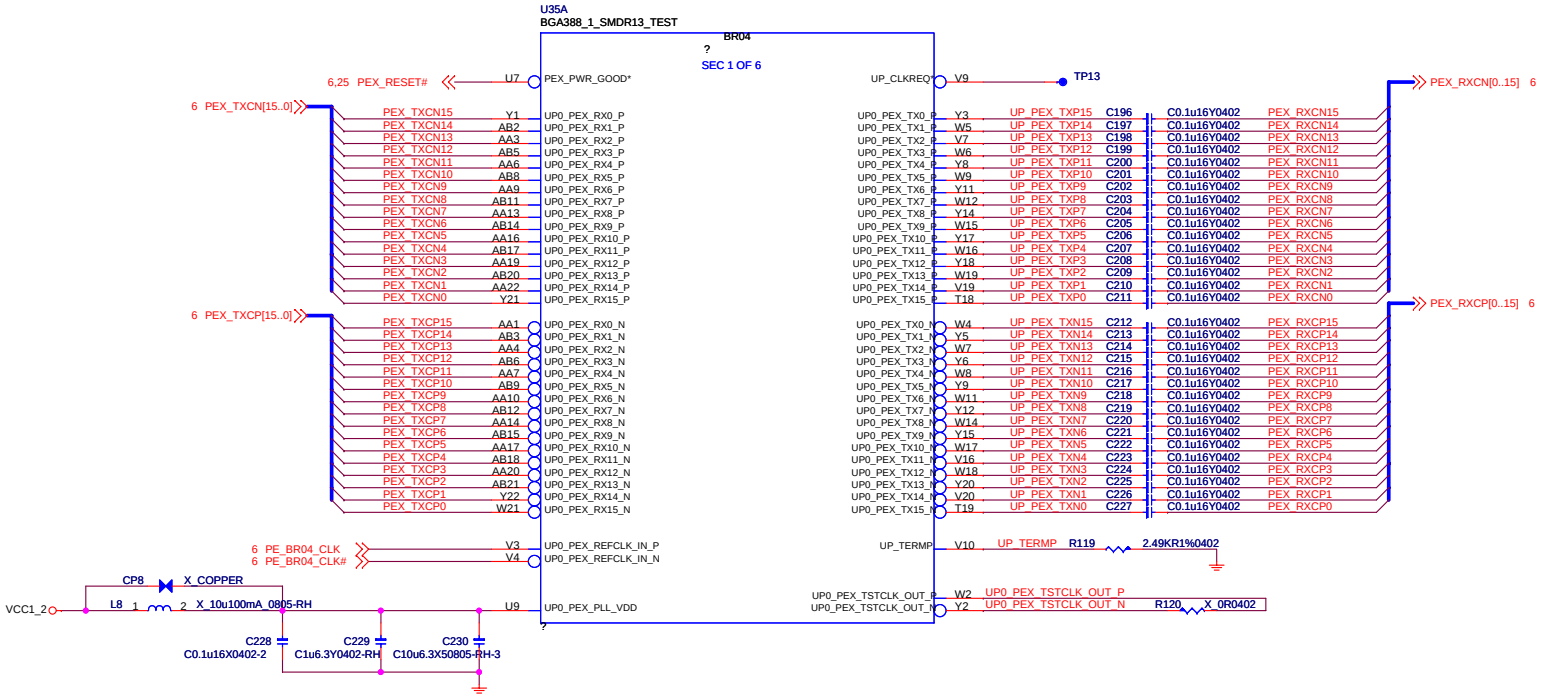
MICRO-START INT'L CO.,LTD.

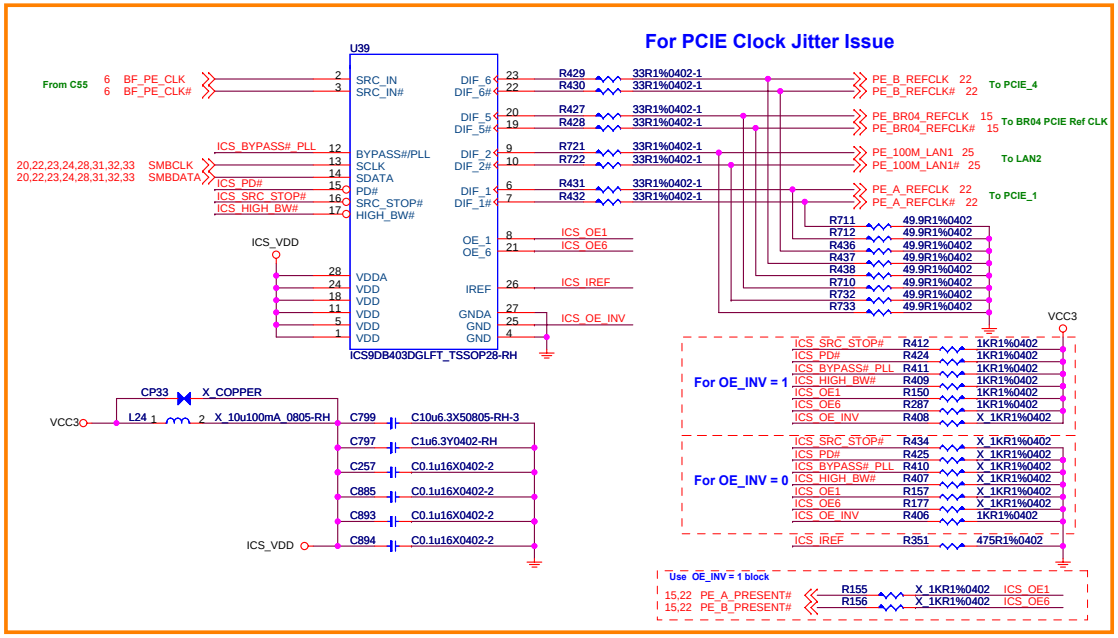
Title: DDR II Termination / EMI

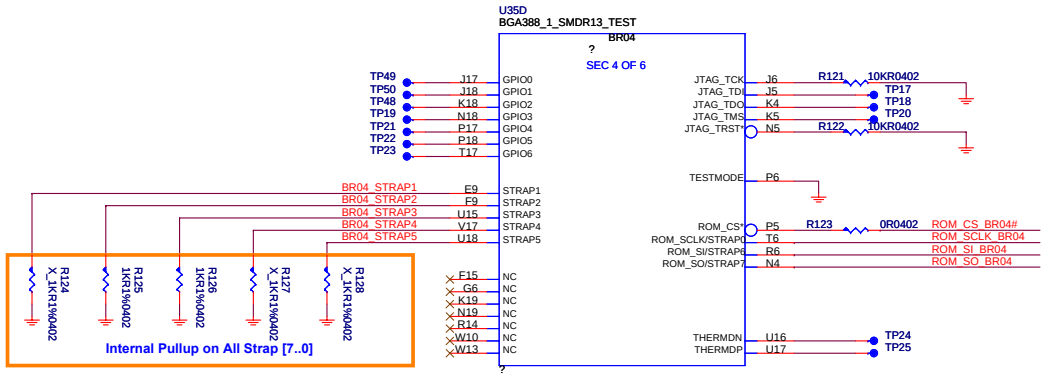
Size	Document Number	Rev
	MS-7510	2.0

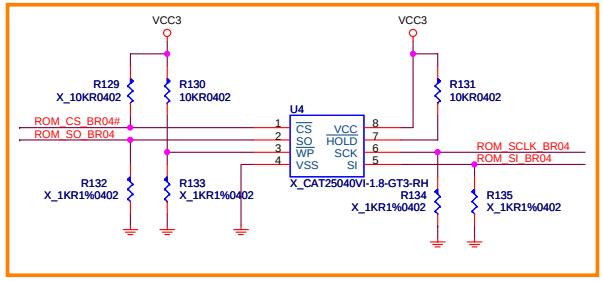
Date: Saturday, January 12, 2008 Sheet 13 of 42

C55 to BR04 PCI-Express Interface









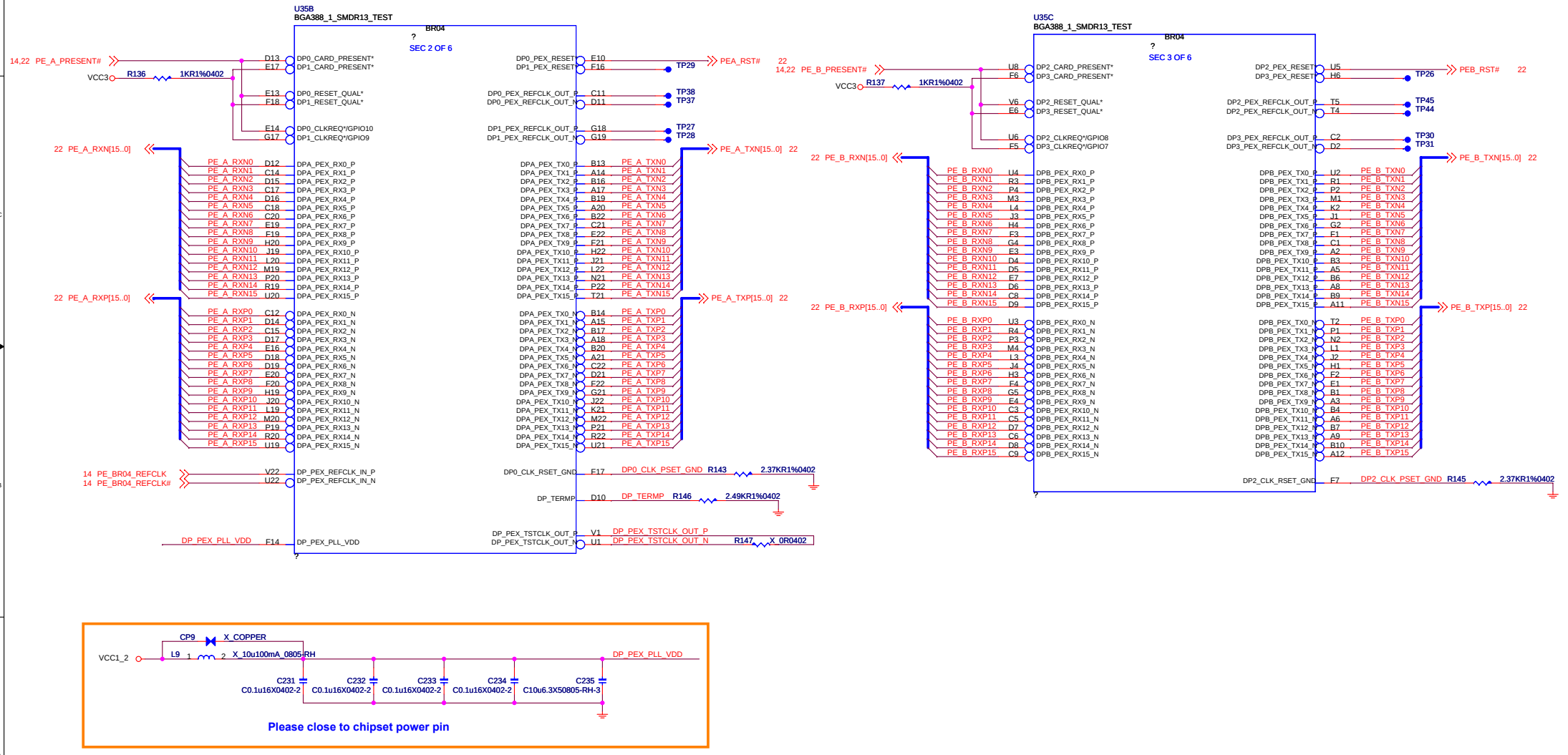
MICRO-START INT'L CO.,LTD.

Title: BR04 - C55 Side

Size	Document Number	Rev
	MS-7510	2.0

Date: Saturday, January 12, 2008 Sheet 14 of 42

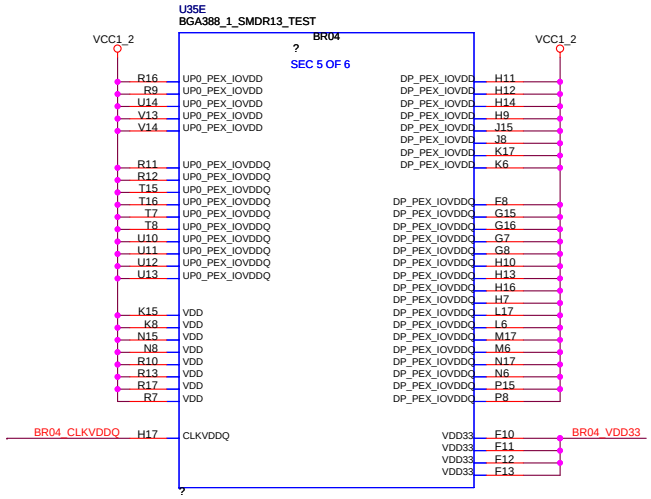
BR04 to PCI-Express Slots

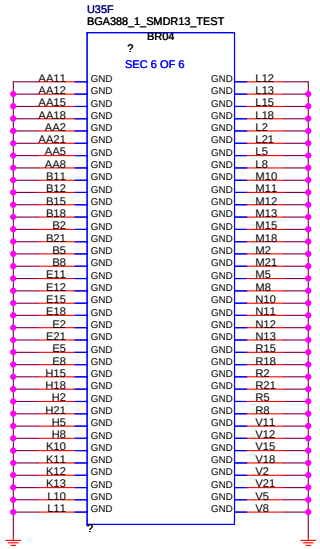


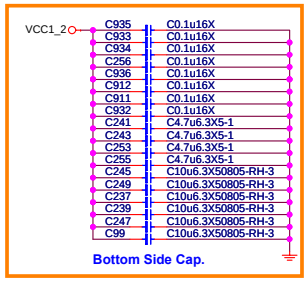
MICRO-START INT'L CO.,LTD.

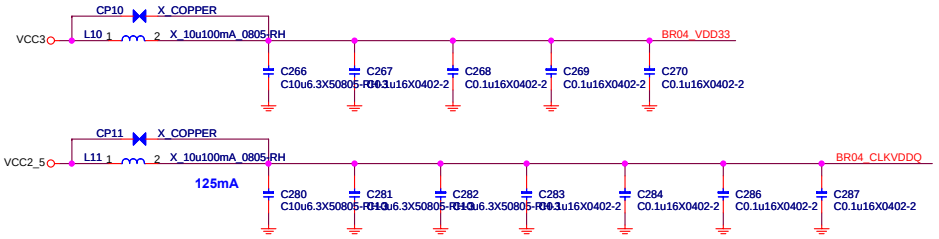
Title: BR04 - PCIE x16 Slot		
Size	Document Number: MS-7510	Rev: 2.0
Date: Sunday, January 20, 2008	Sheet 15 of 42	

BR04 Power and Gnd Block





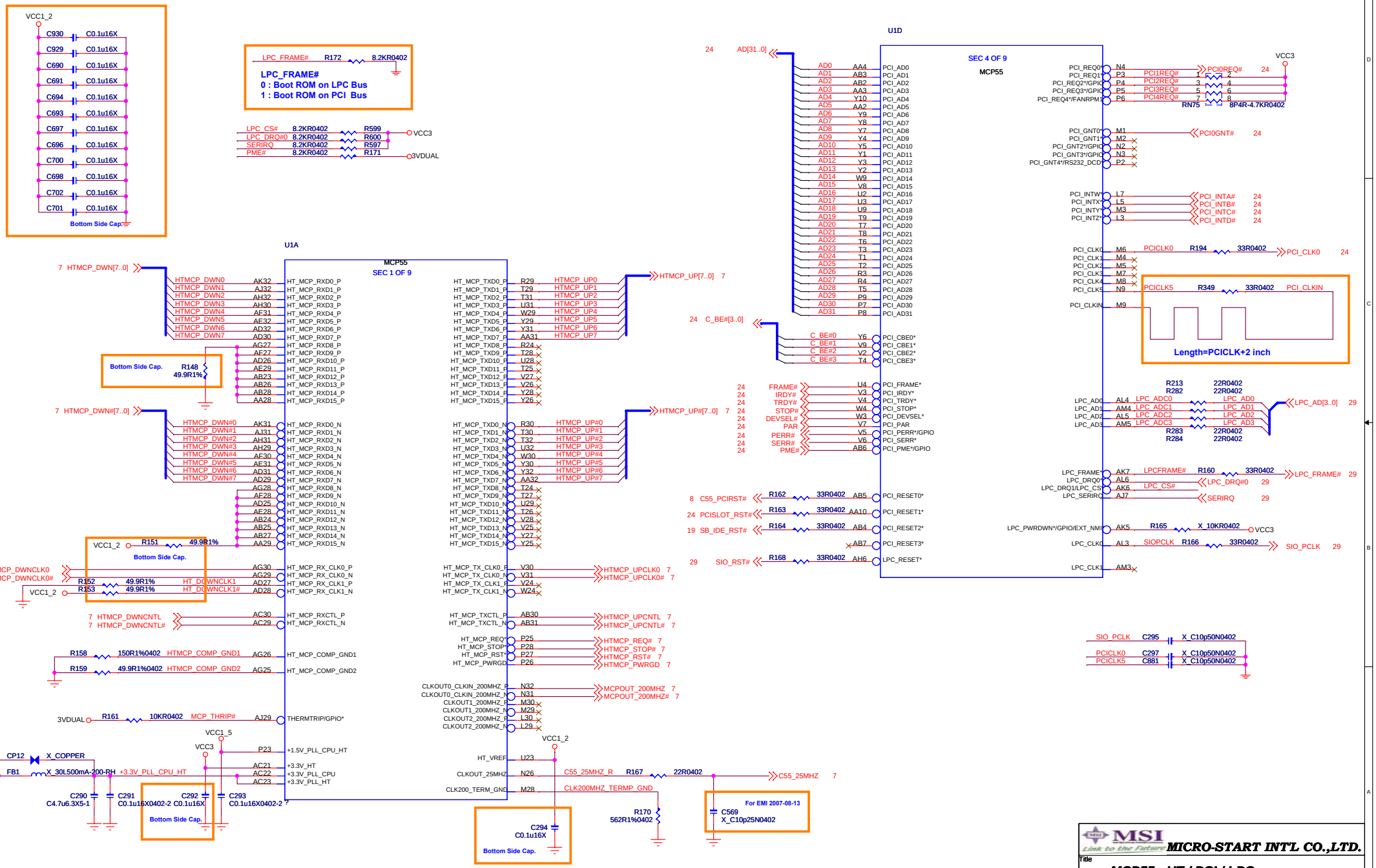




MICRO-START INT'L CO.,LTD.

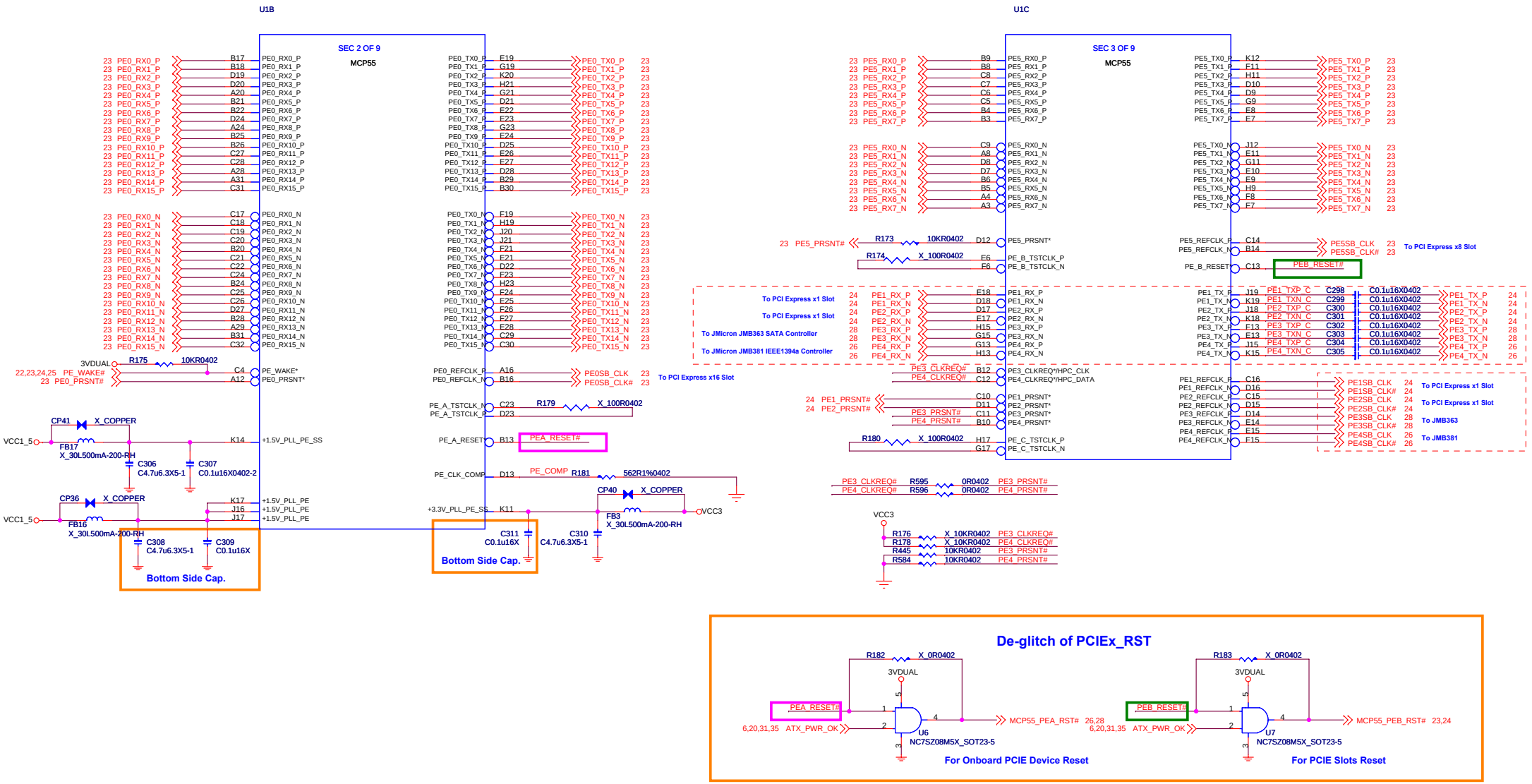
Title: BR04 - Power / Gnd		
Size	Document Number: MS-7510	Rev: 2.0
Date: Sunday, January 20, 2008	Sheet 16 of 42	

MCP55 - HT / PCI / LPC



MSI Link to the Future	MICRO-START INT'L CO.,LTD.		
Title	MCP55 - HT / PCI / LPC		
Size	Document Number	MS-7510	Rev 2.0
Date: Sunday, January 20, 2008	Sheet 17 of 42		

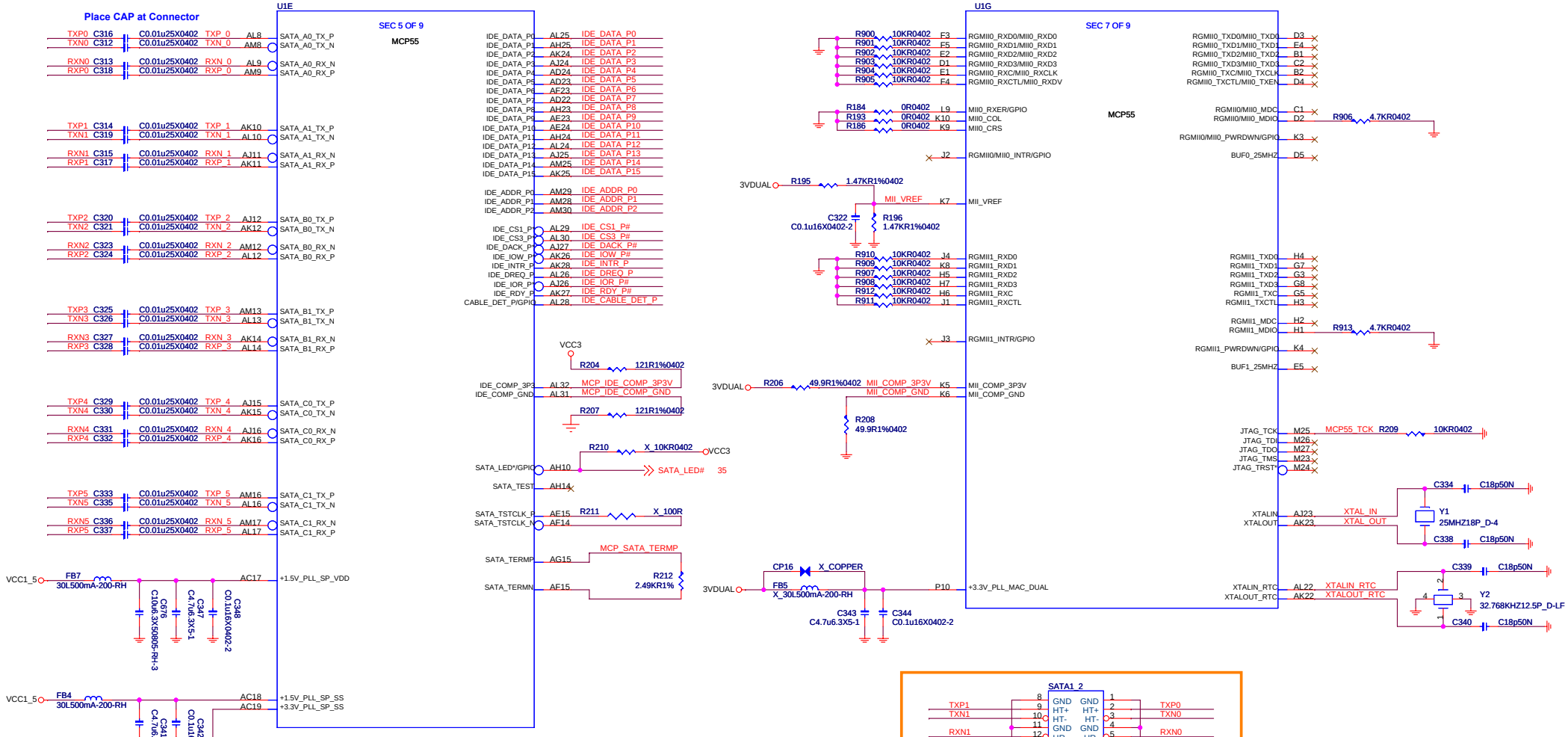
MCP55 - PCI-E



MICRO-START INT'L CO.,LTD.

Title	MCP55 - PCI-E	
Size	Document Number MS-7510	Rev 2.0
Date: Monday, January 14, 2008	Sheet 18 of 42	

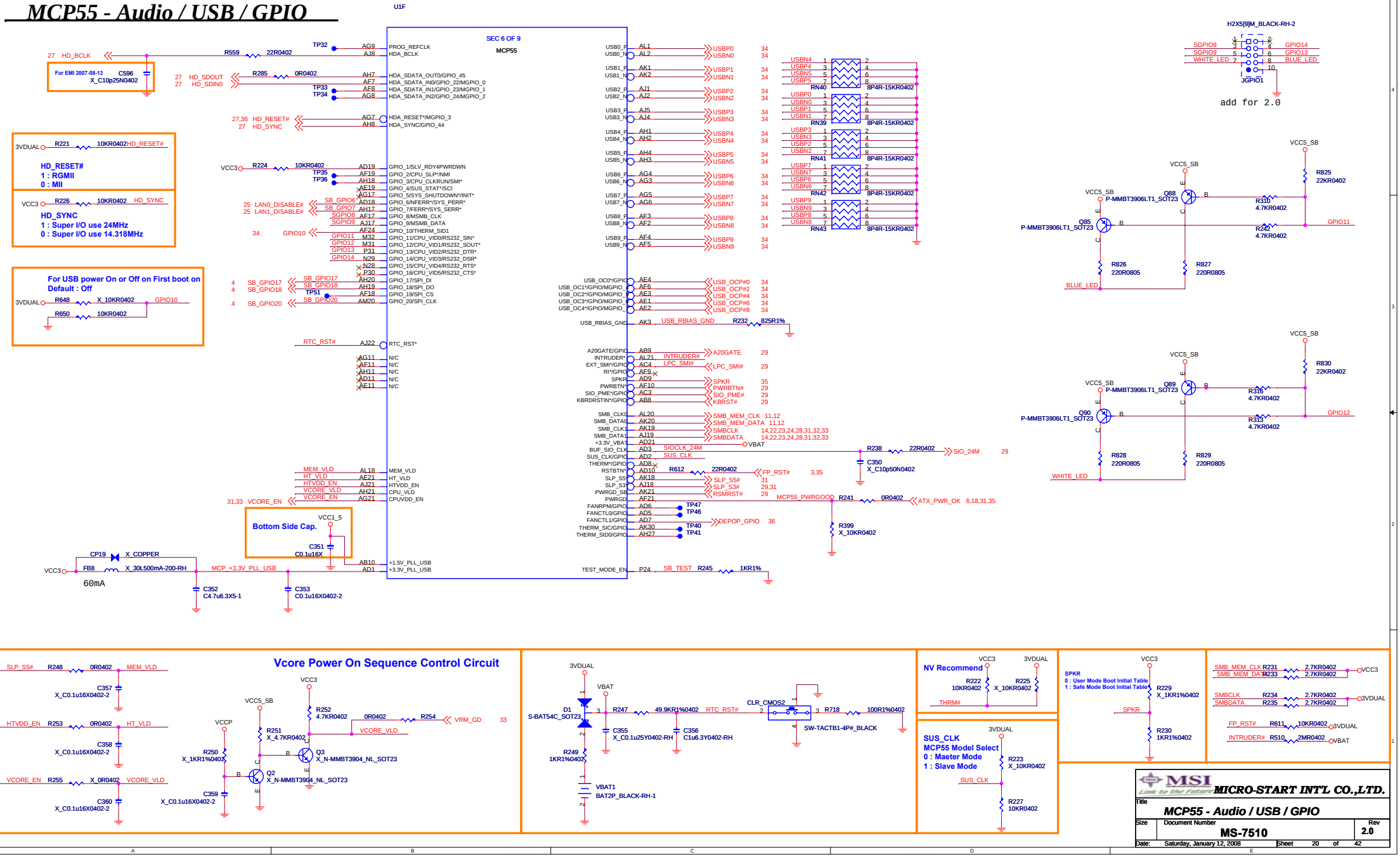
MCP55 - SATA / IDE / RGMII



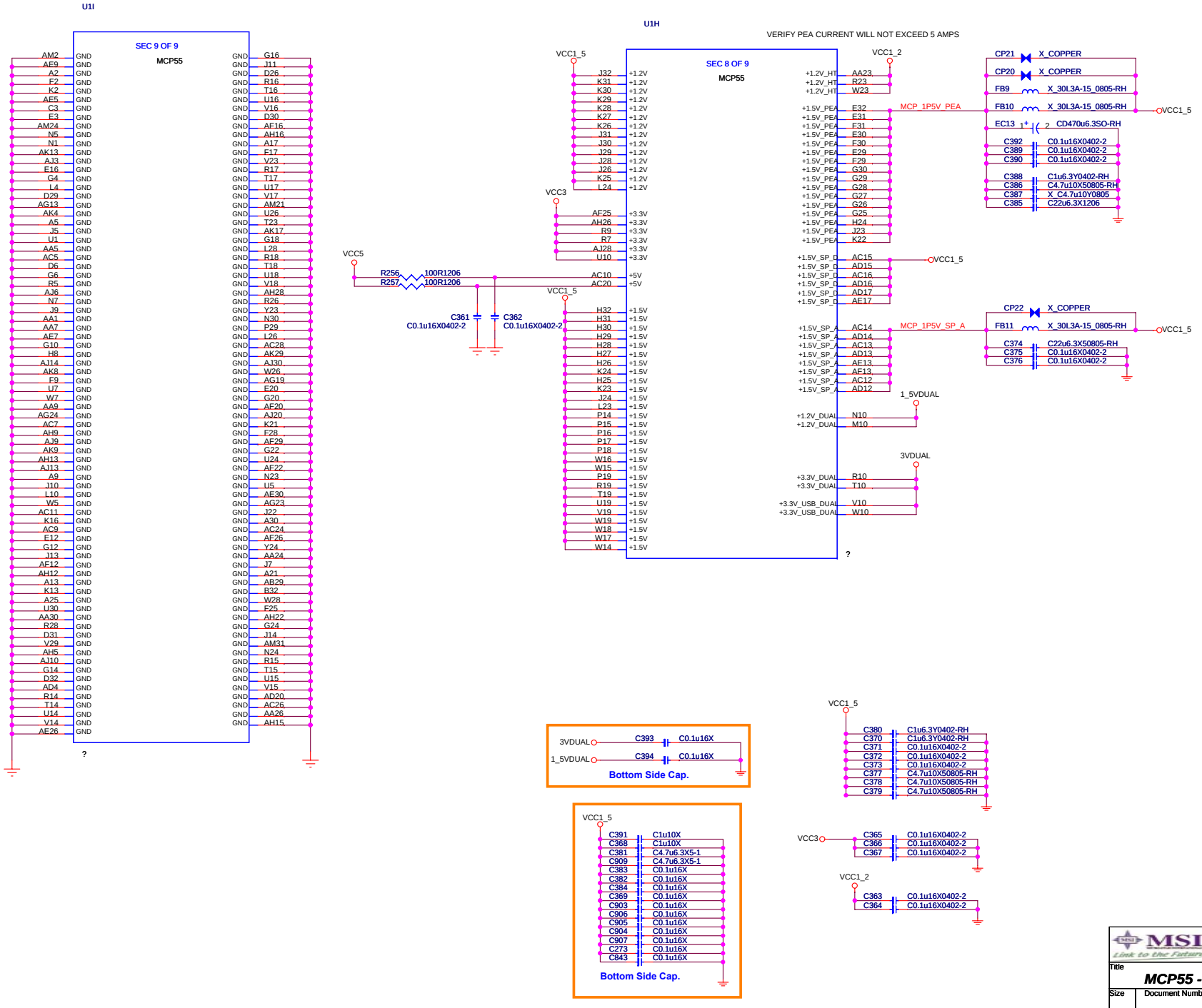
MICRO-START INT'L CO.,LTD.

Title	MCP55 - SATA / IDE / RGMII	
Size	Document Number MS-7510	Rev 2.0
Date: Saturday, January 12, 2008	Sheet 19 of 42	

MCP55 - Audio / USB / GPIO



MCP55 - Power & Gnd



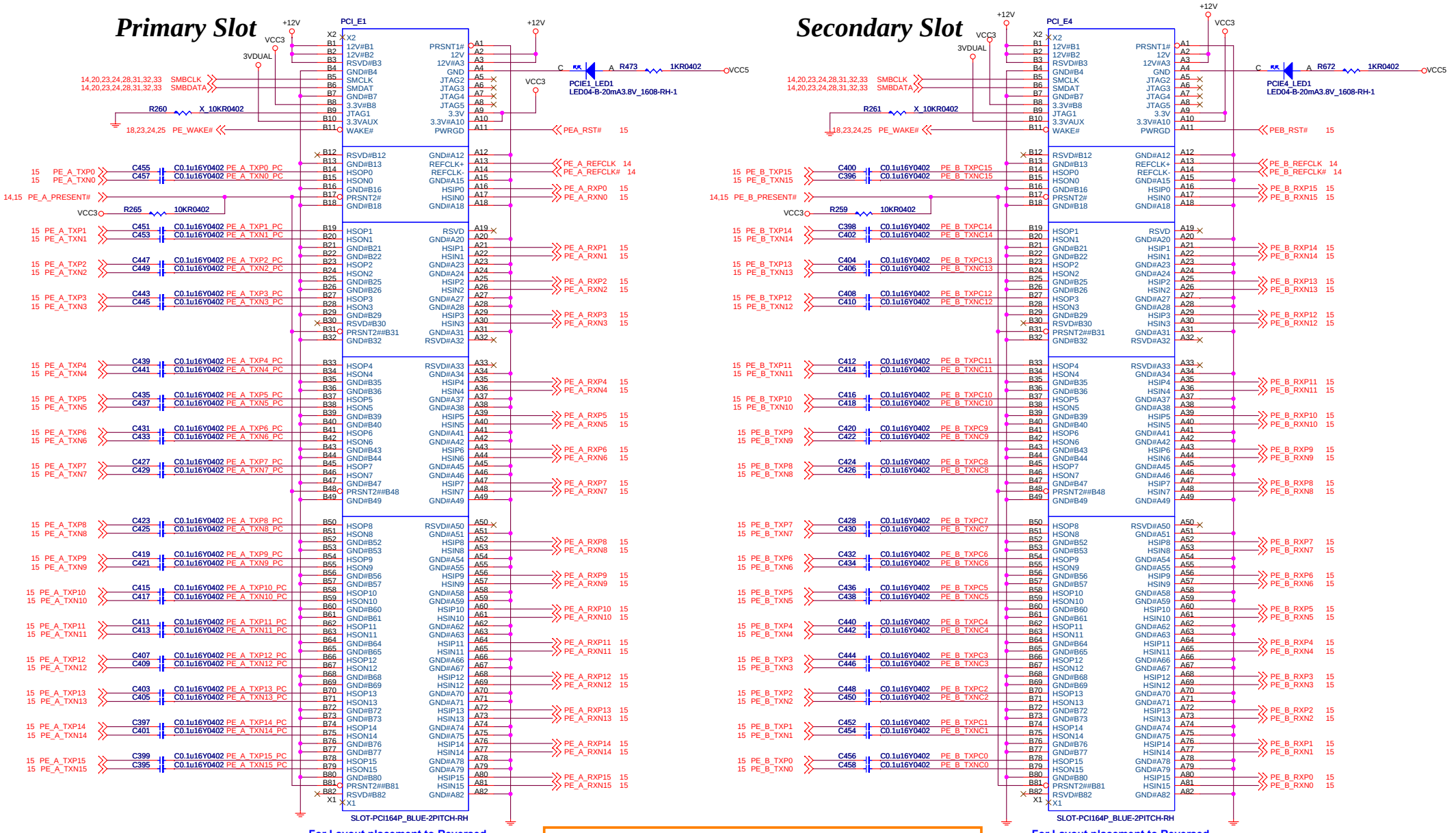
MSI MICRO-START INT'L CO.,LTD.

Title: MCP55 - Power & Gnd

Size	Document Number	Rev
	MS-7510	2.0

Date: Sunday, January 20, 2008 Sheet 21 of 42

PCI-Express x16 Primary and Secondary Slot



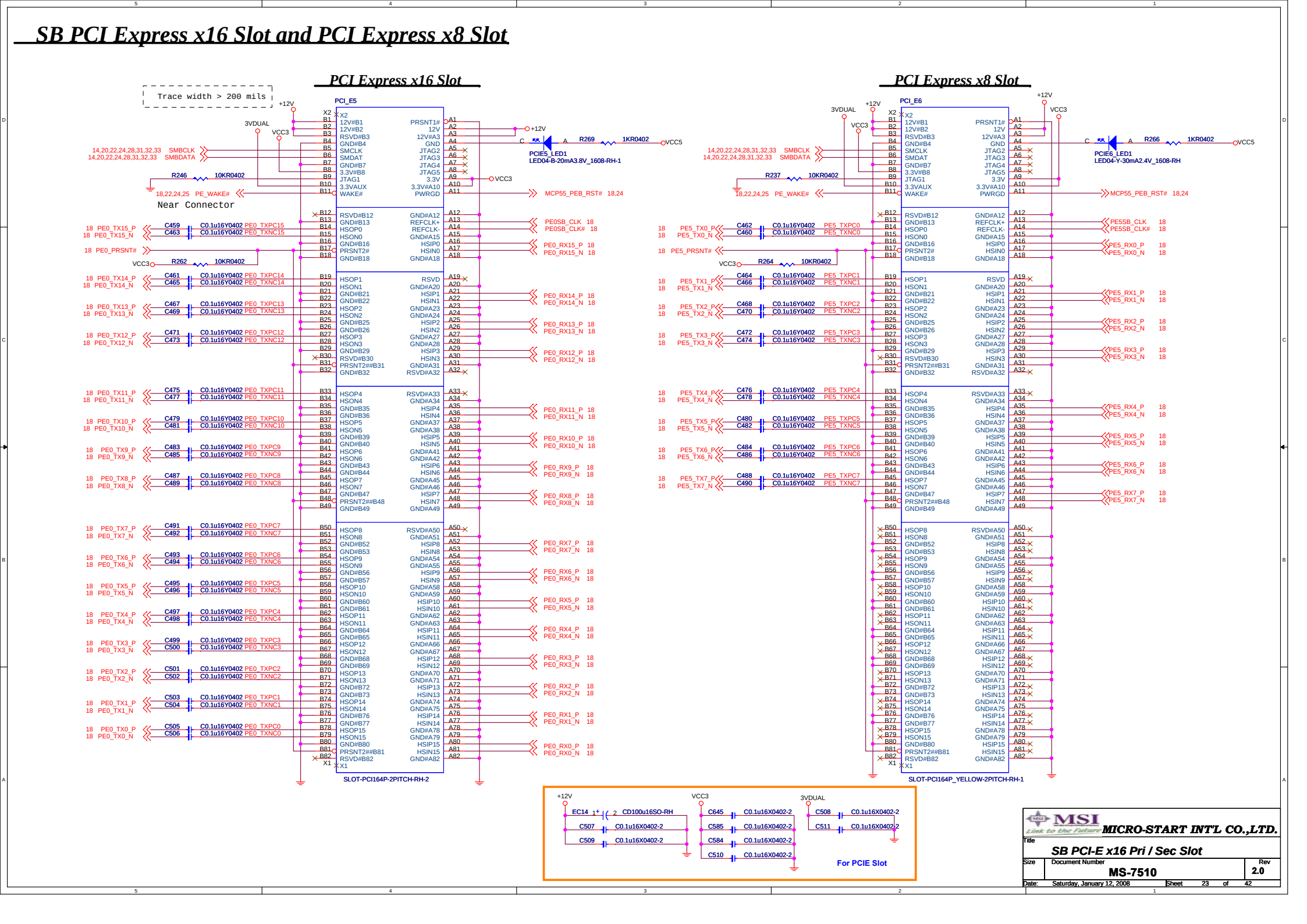
MICRO-START INT'L CO.,LTD.

Title: NB PCI-E x16 Pri / Sec Slot

Size	Document Number	Rev
	MS-7510	2.0

Date: Saturday, January 12, 2008 Sheet 22 of 42

SB PCI Express x16 Slot and PCI Express x8 Slot

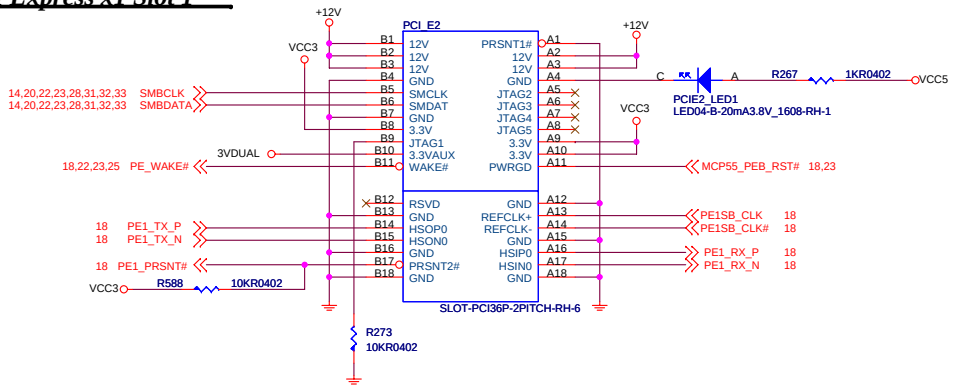


MSI MICRO-START INT'L CO.,LTD.

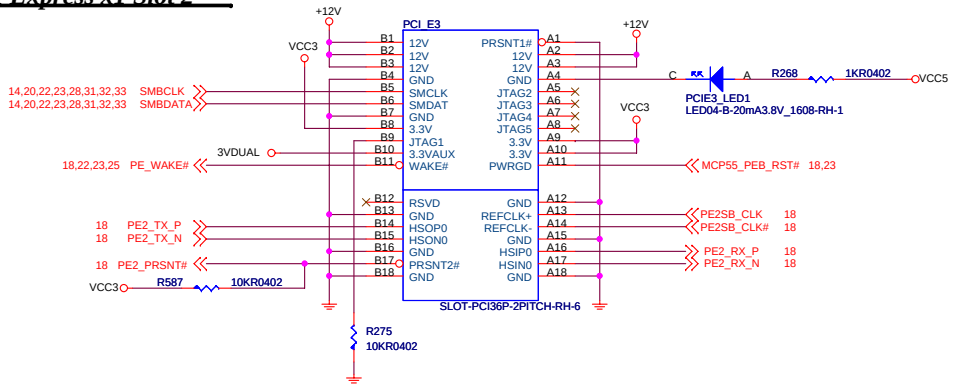
Title	SB PCI-E x16 Pri / Sec Slot	
Size	Document Number MS-7510	Rev 2.0
Date: Saturday, January 12, 2008	Sheet 23 of 42	

SB PCI-Express x1 Slots and PCI Slot

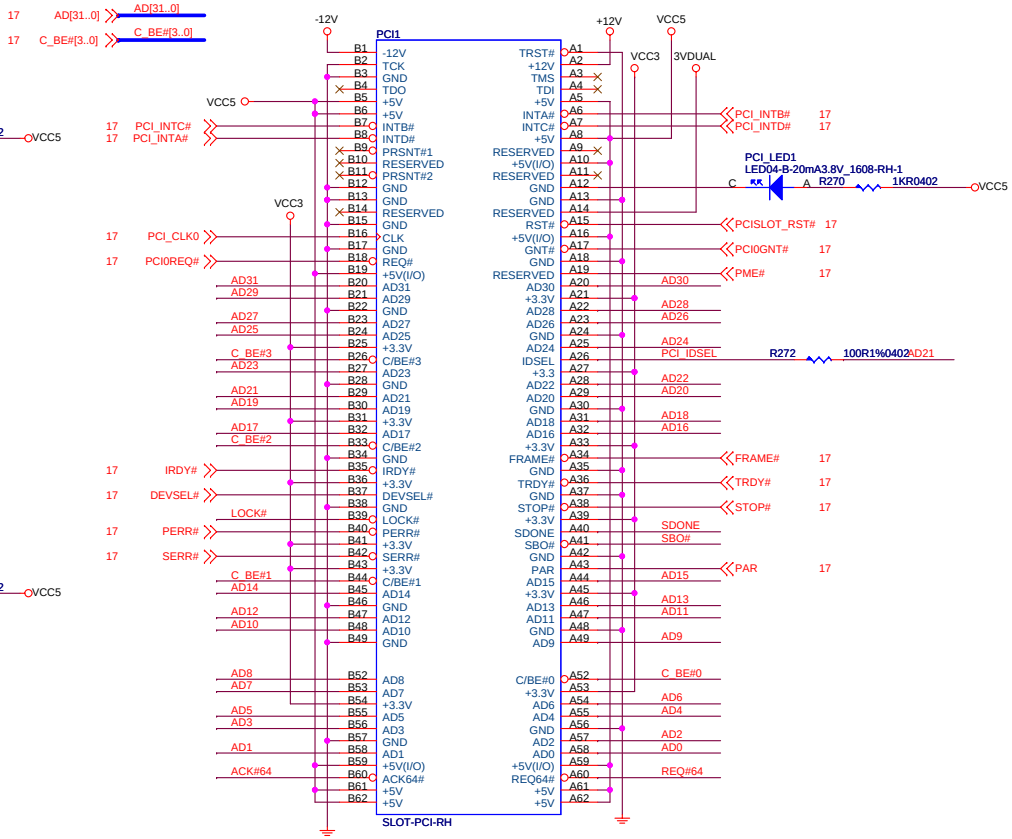
PCI Express x1 Slot 1



PCI Express x1 Slot 2

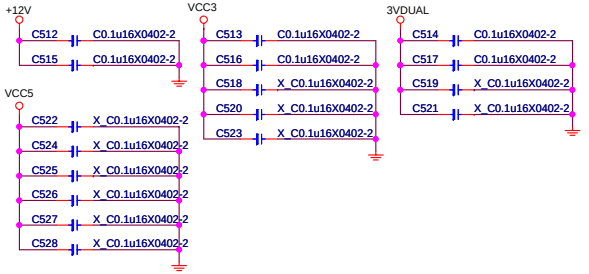


PCI Slot 1 (PCI VER: 2.2 Comply)

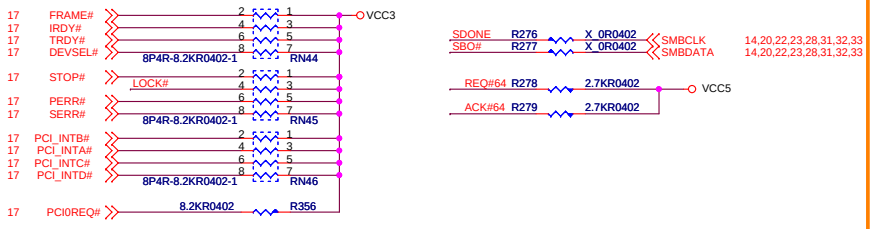


IDSEL = AD21
MASTER = PCI0REQ#
PCI0GNT*

PCI Slot Decoupling Capacitors



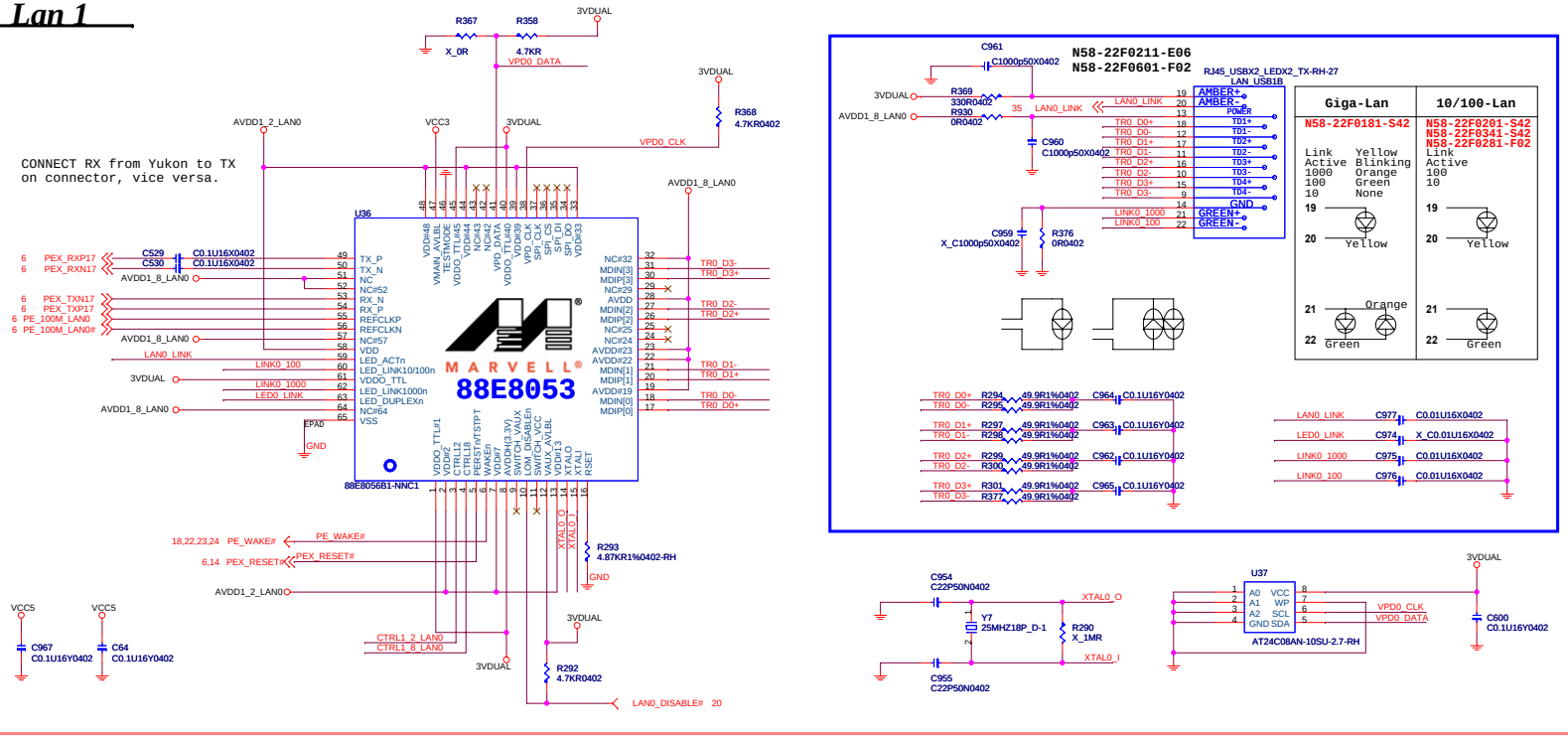
PCI Pull Up / Down Resistors

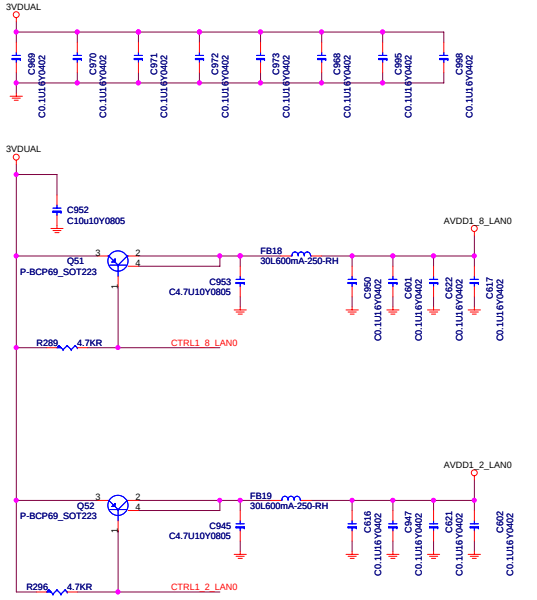


MICRO-START INT'L CO.,LTD.

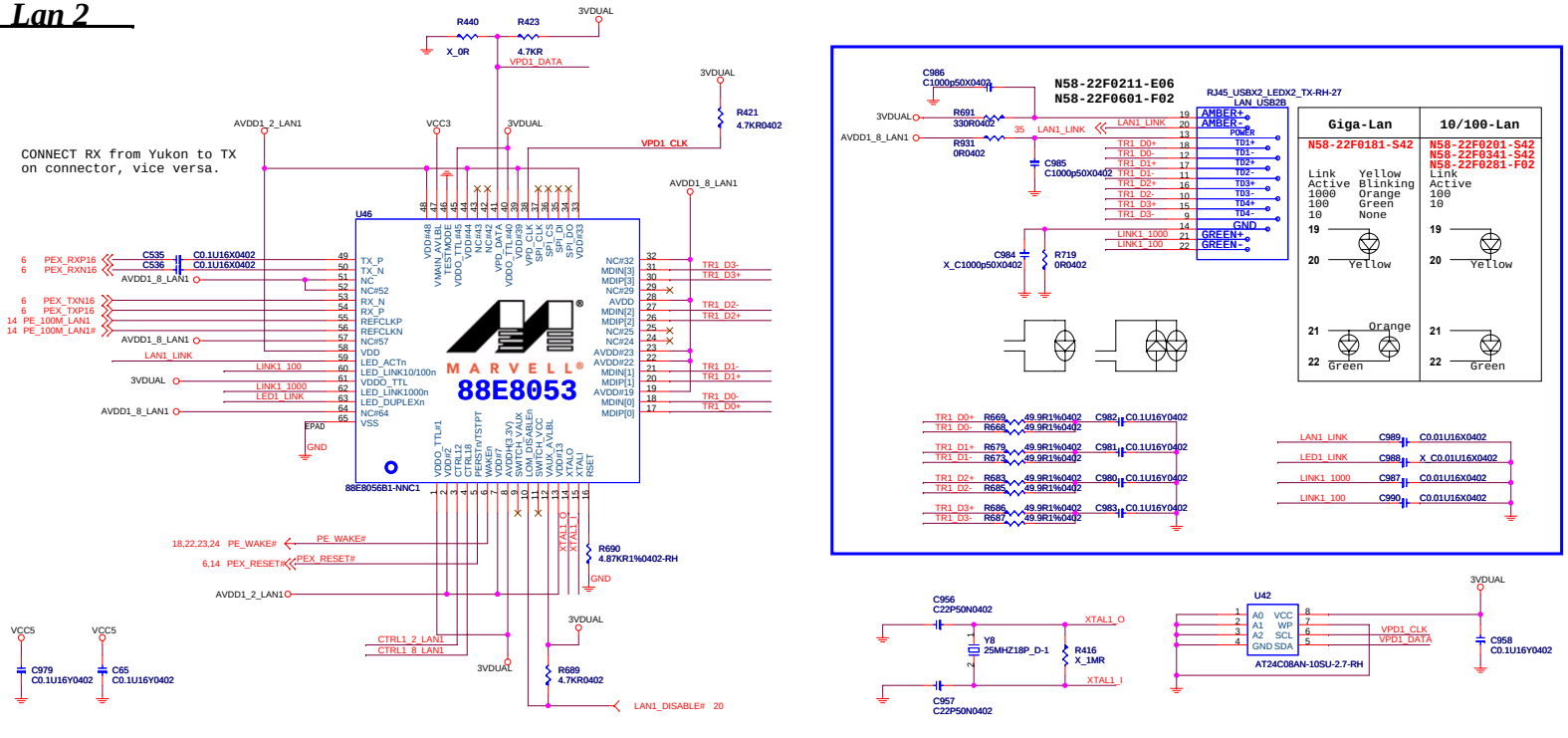
Title	SB PCI-E x1 / PCI Slot	
Size	Document Number MS-7510	Rev 2.0
Date: Saturday, January 12, 2008	Sheet 24 of 42	

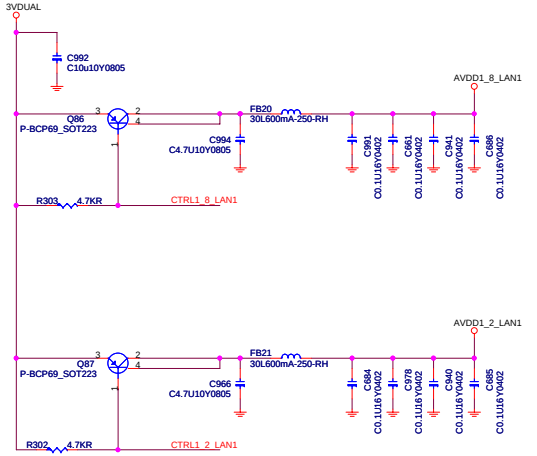
Lan 1

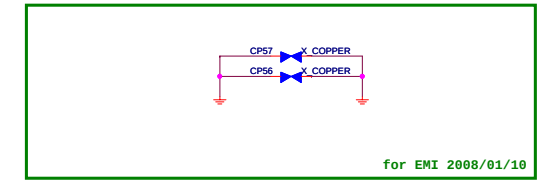




Lan 2



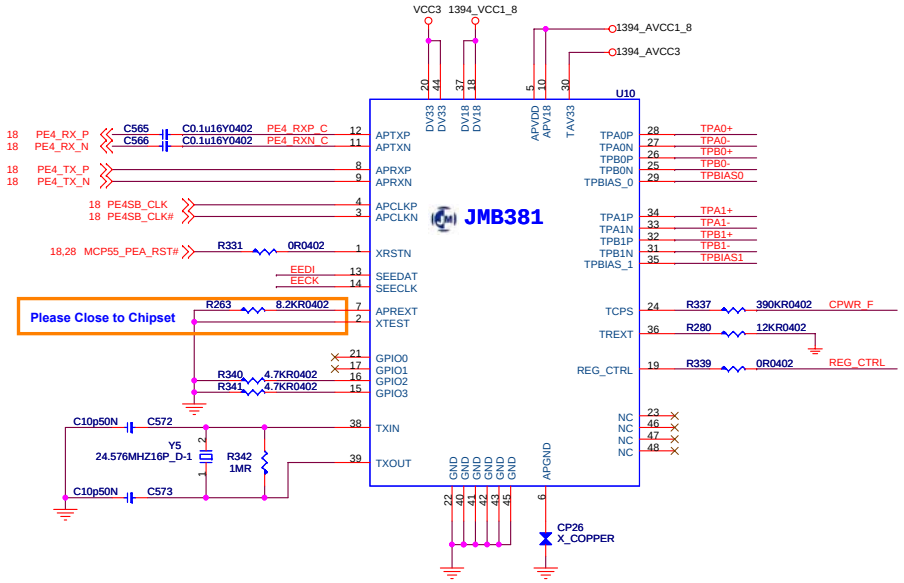


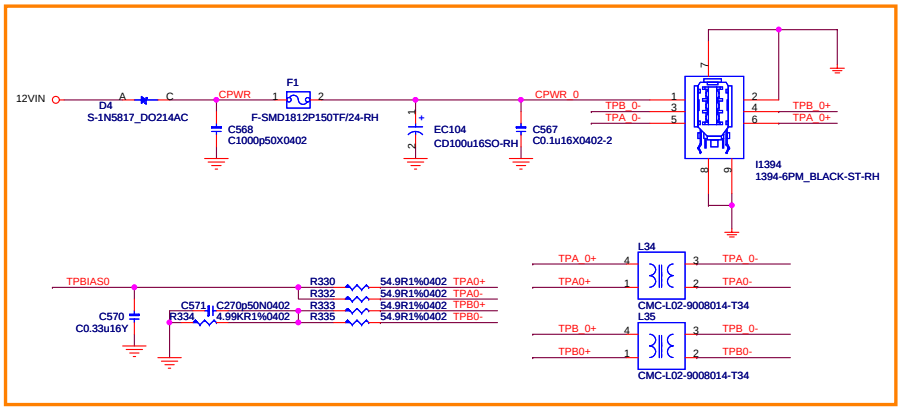


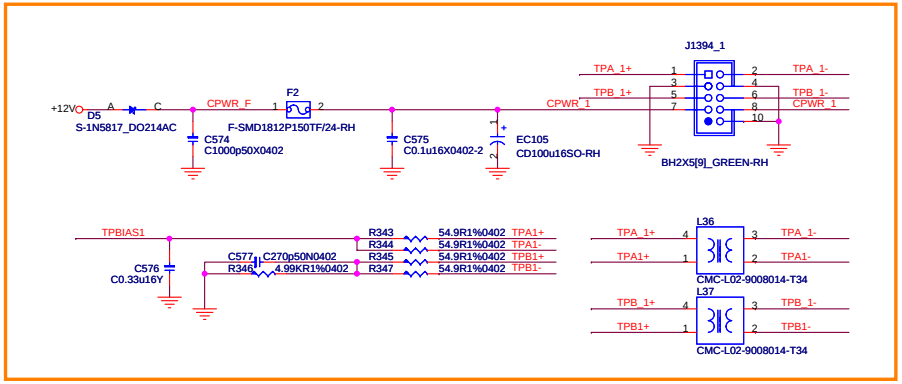
MSI MICRO-START INT'L CO.,LTD.

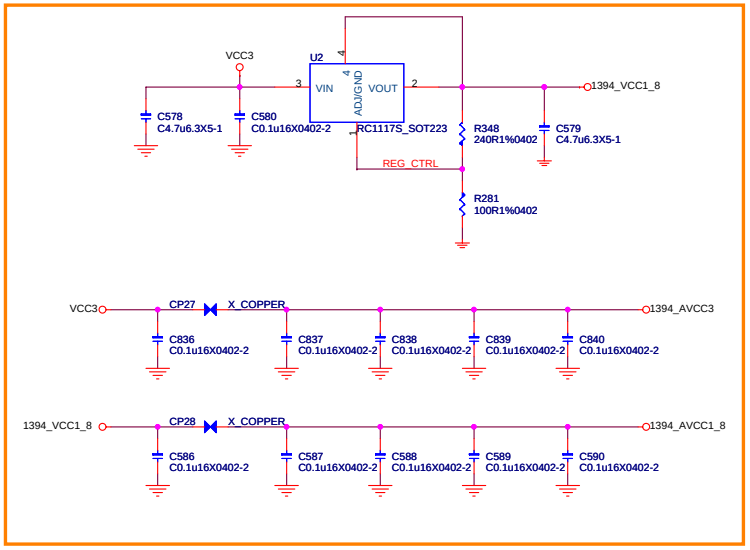
Title	Gigabit LAN - MARVELL/88E8056	
Size	Document Number: MS-7510	Rev: 2.0
Date: Saturday, January 12, 2008	Sheet 25 of 42	

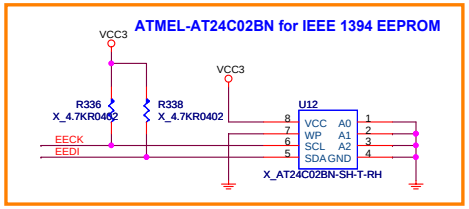
PCIE To 1394a OHCI Host Controller - JMB381







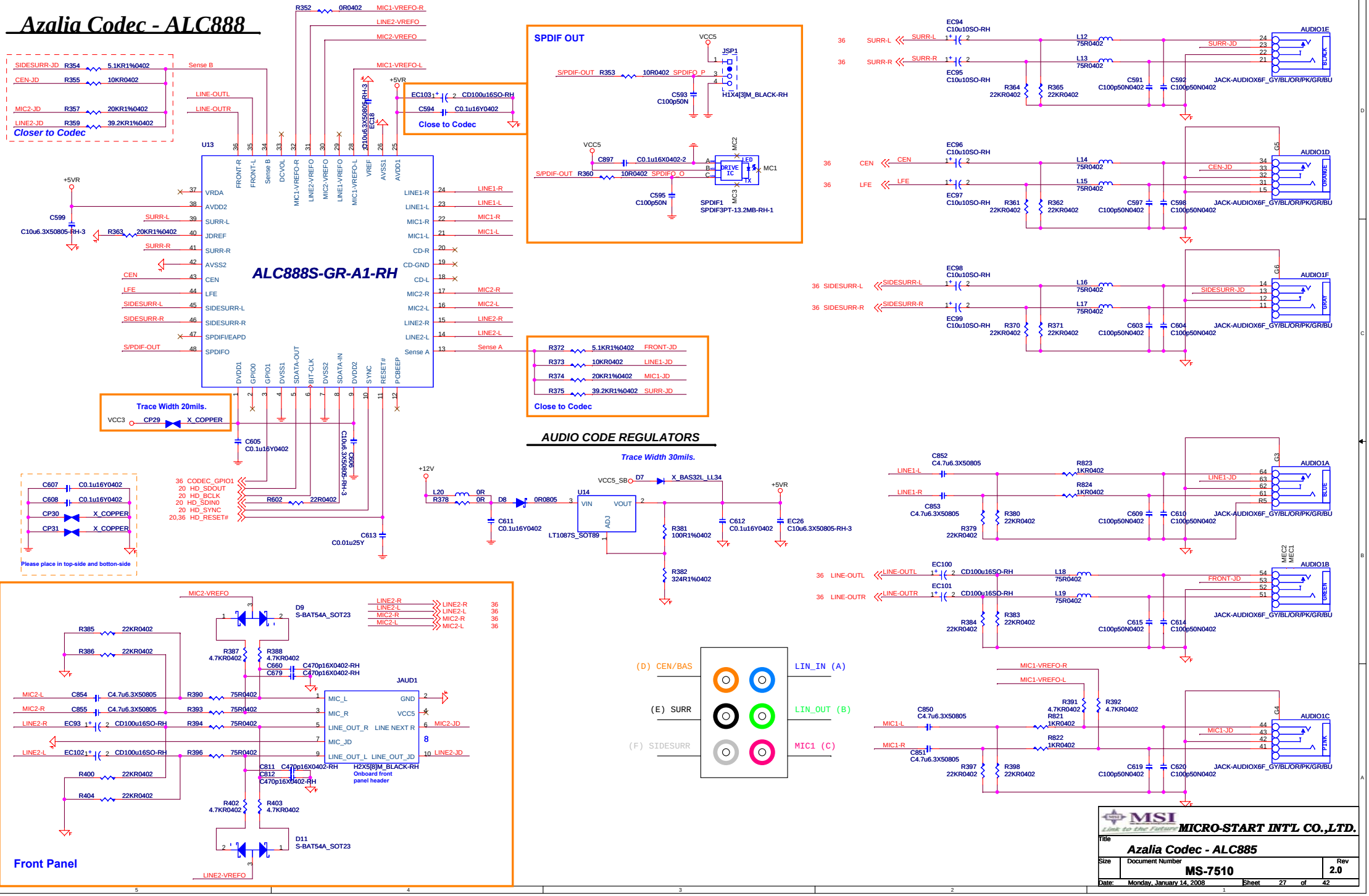




MICRO-START INT'L CO.,LTD.

Title	1394 Controller - JMB381	
Size	Document Number MS-7510	Rev 2.0
Date: Saturday, January 12, 2008	Sheet 26 of 42	

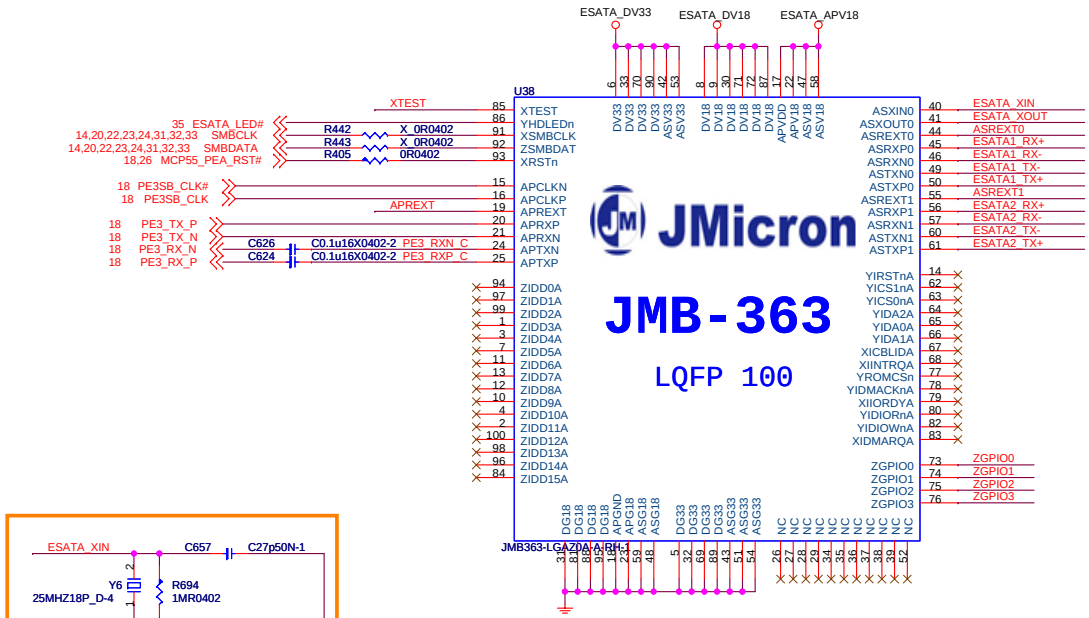
Azalia Codec - ALC888

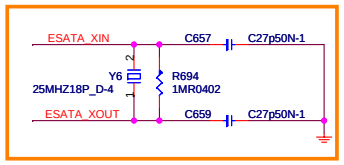


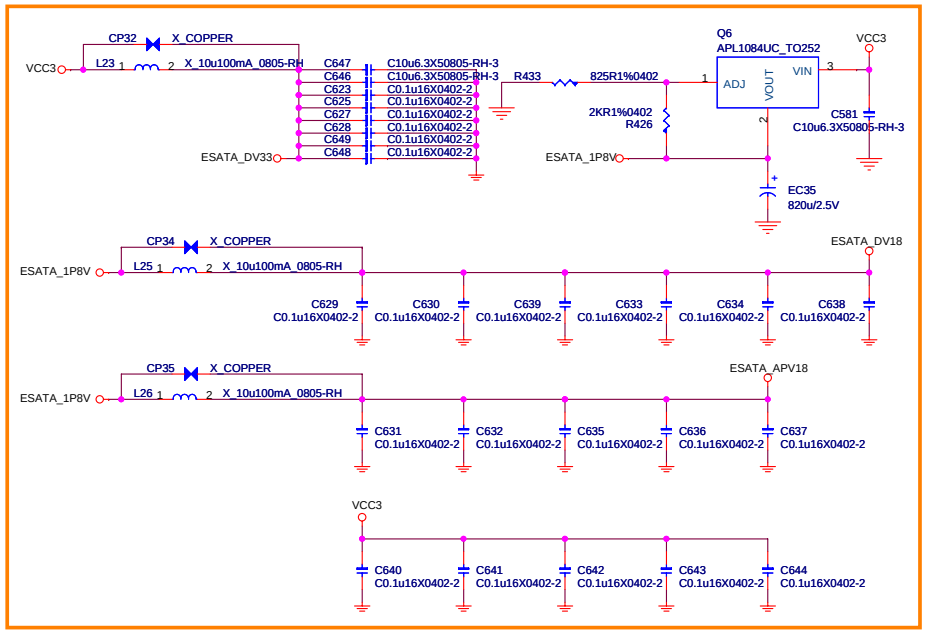
MSI Link to the Future **MICRO-START INT'L CO.,LTD.**

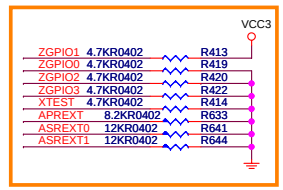
Title	Azalia Codec - ALC885	
Size	Document Number MS-7510	Rev 2.0
Date: Monday, January 14, 2008	Sheet 27 of 42	

JMicron JMB363 eSATA Controller







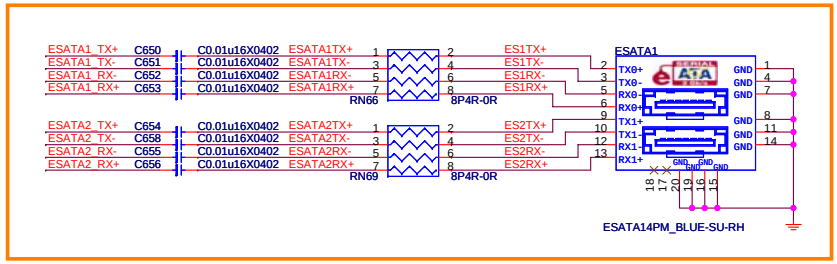


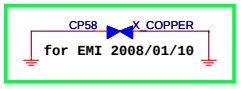
JMB363 GPIO0
It uses to control function# available on JMB363.
0: single function ; 1: multi-function
JMB363 GPIO1
It uses to control clock source of SATA II port 0.
0: from internal clock source from PCI Express clock source
1: from ASXIN0 & ASXOUT0
JMB363 GPIO2
It uses to control interface to access internal debug registers.
0: SMBus I/F ; 1: Reserved for debugging.
JMB363 GPIO3
Reserved for debugging.
JMB363 Test Mode Enable.
High-active signal to enable testing and debug modes of JMB363.

SATA II Port 0 External Reference Resistor.
An external 12KΩ±1%resistor should be connected and bypass to the ground ASG18 (pin#48).

SATA II Port 0 External Reference Resistor.
An external 12KΩ±1%resistor should be connected and bypass to the ground ASG18 (pin#59).

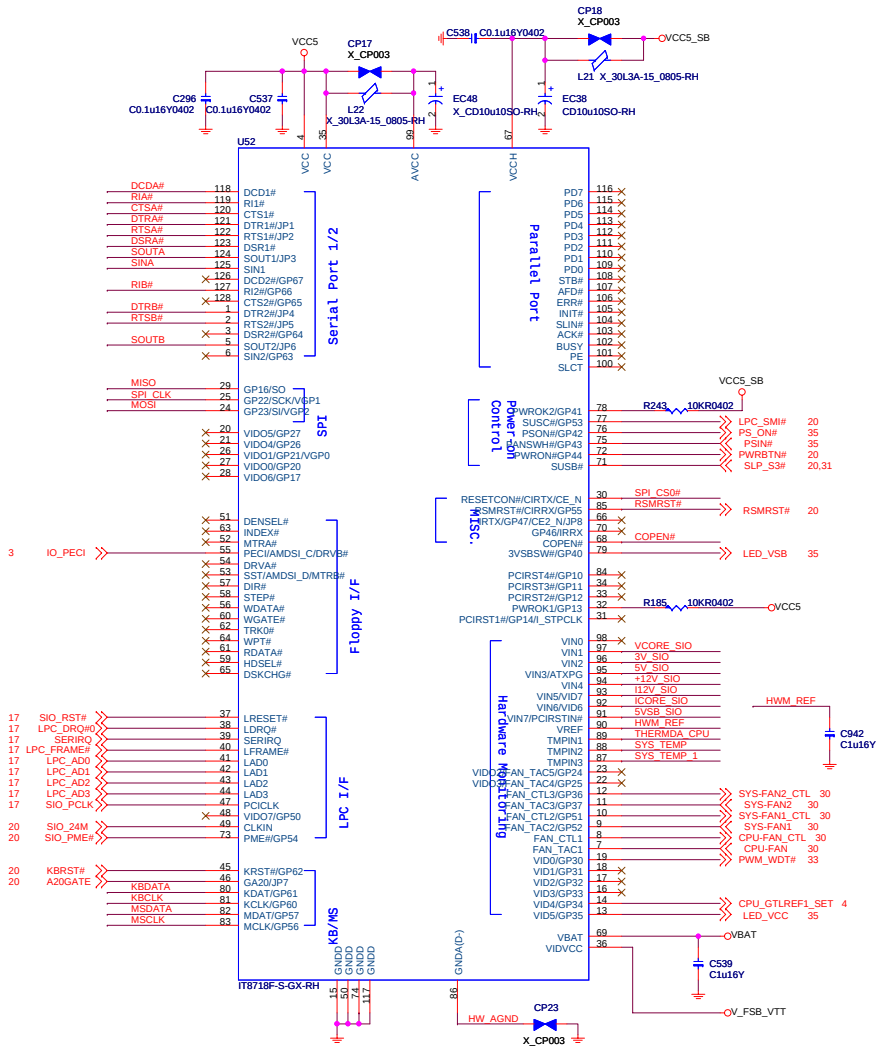
PCI Express External Reference Resistor.
An external 12KΩ±1% resistor should be connected and bypass to the ground APG18 (pin#18).

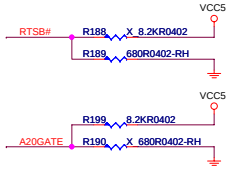




MSI MICRO-START INT'L CO.,LTD.

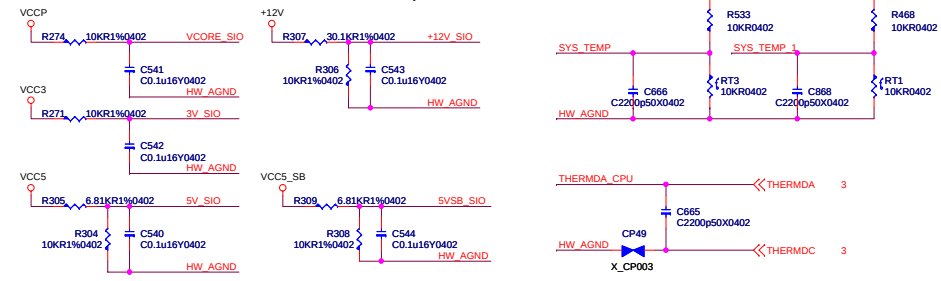
Title	JMicron JMB363 eSATA Controller	
Size	Document Number MS-7510	Rev 2.0
Date: Saturday, January 12, 2008	Sheet 28 of 42	

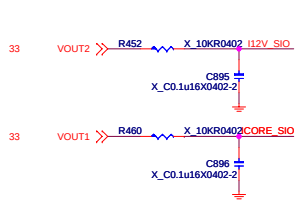




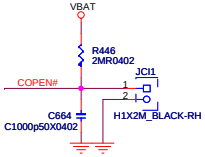
RTSB#	A20GATE	FAN DUTY
1	1	100%
1	0	75%
0	1	50%
0	0	25%

Voltage and Temperture H/W Monitor Block

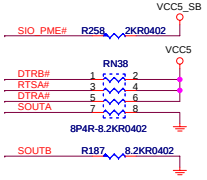




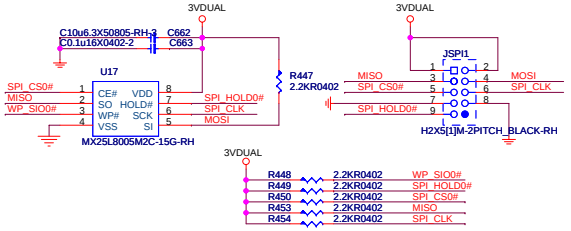
Chasiss Intrusion

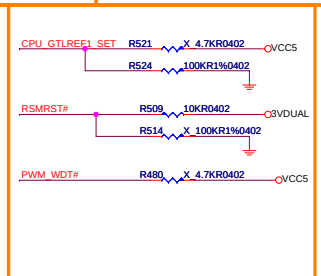


Boot Setup Strapping

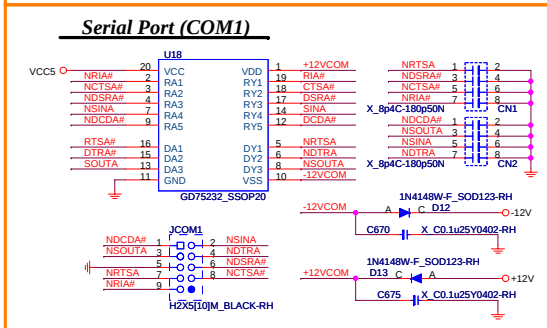


SPI Flash ROM and Debug Port

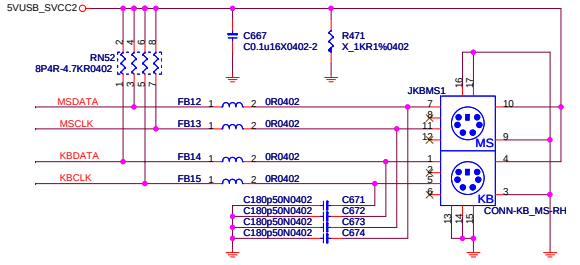


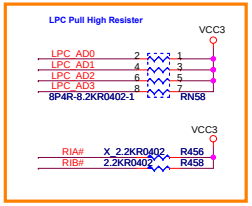


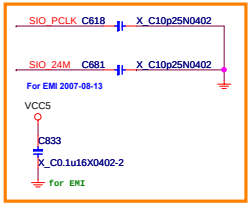
Serial Port (COM1)



PS2 Keyboard and Mouse Connector







MSI Link to the Future MICRO-START INT'L CO.,LTD.

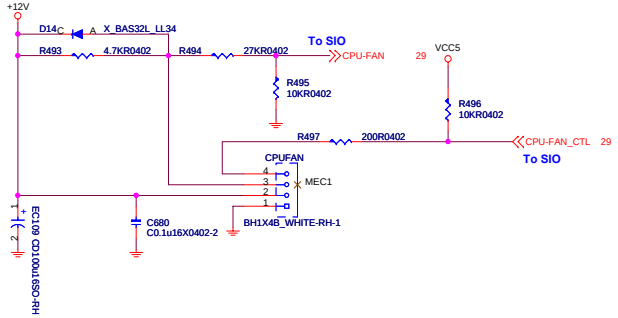
Title: Super I/O - F71882FG

Size | Document Number: MS-7510 | Rev: 2.0

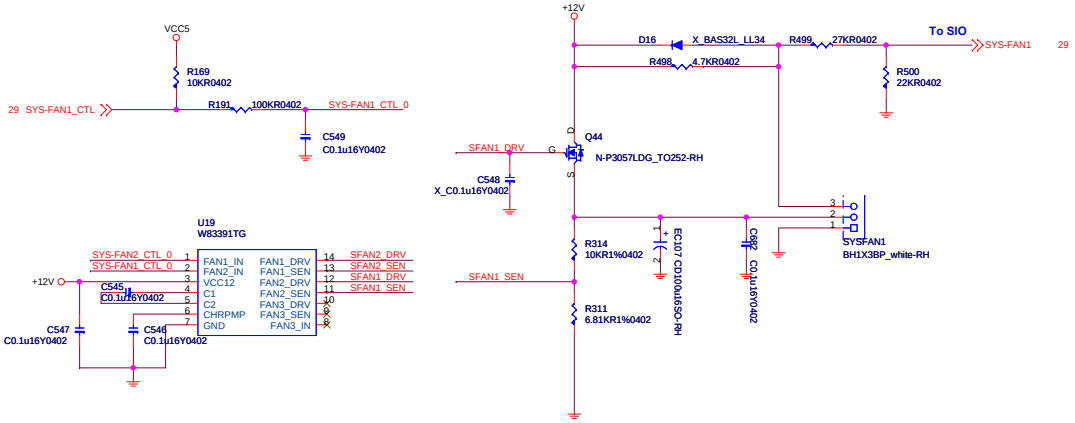
Date: Monday, January 14, 2008 | Sheet 29 of 42

Fan Controller

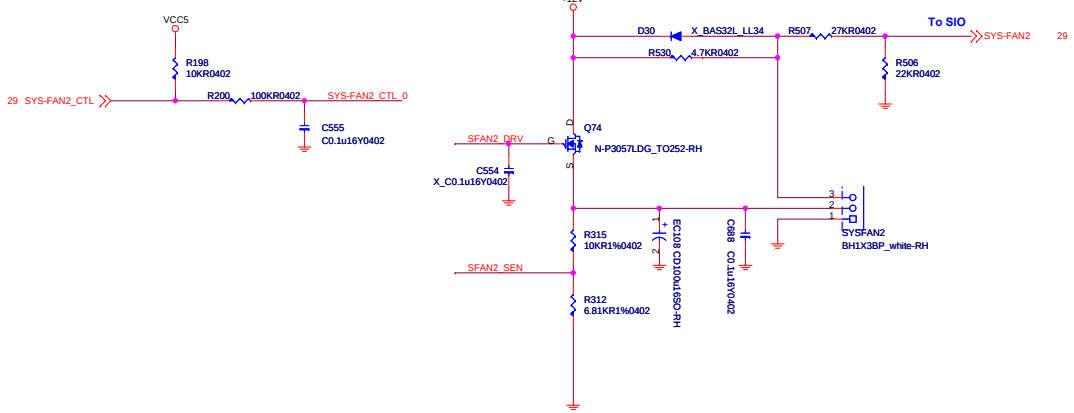
CPU Fan



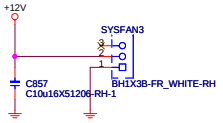
System Fan 1



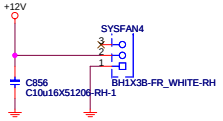
System Fan 2



System Fan 3

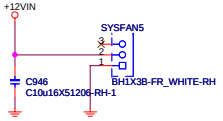


System Fan 4



Near the CPU

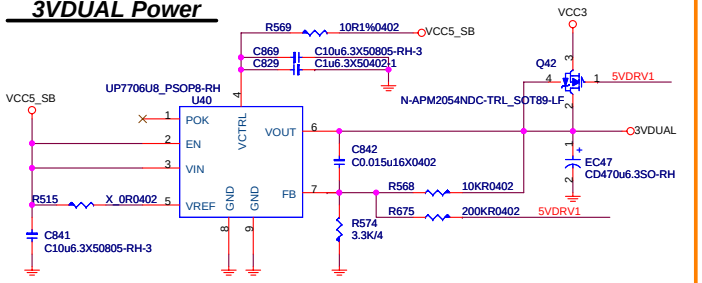
System Fan 5



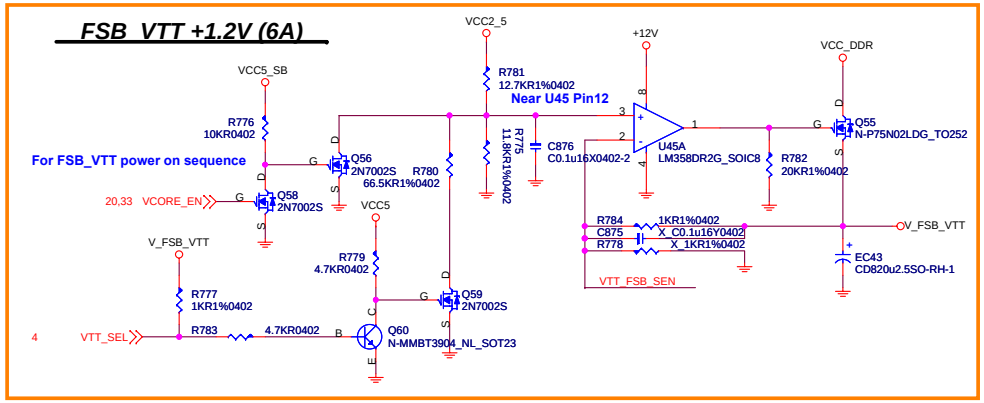
MSI Link to the Future	MICRO-START INT'L CO.,LTD.	
Title: Fan Controller		
Size	Document Number: MS-7510	Rev: 2.0
Date: Monday, January 14, 2008	Sheet 30 of 42	

uPI ACPI Solution

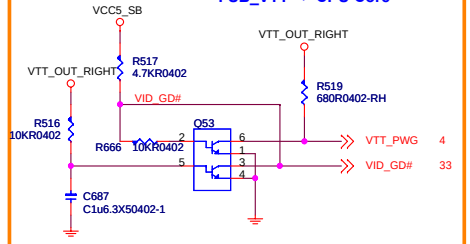
3VDUAL Power



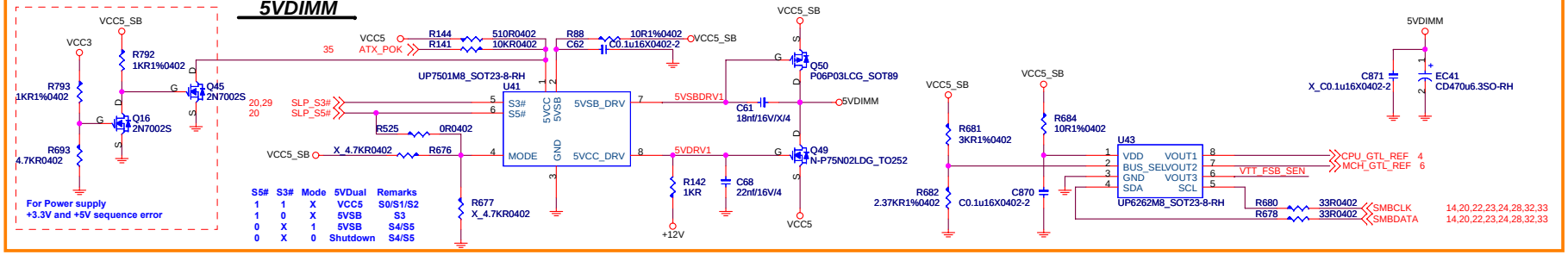
FSB_VTT +1.2V (6A)



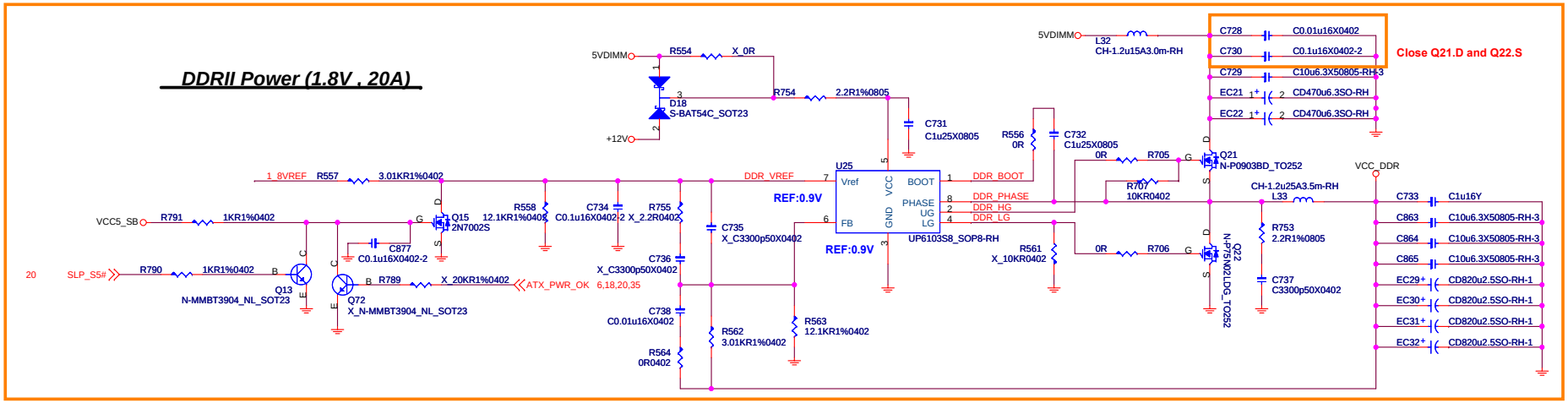
Power on sequence FSB_VTT => CPU Core



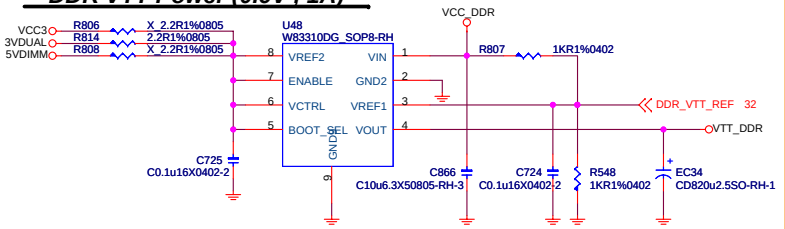
5VDIMM



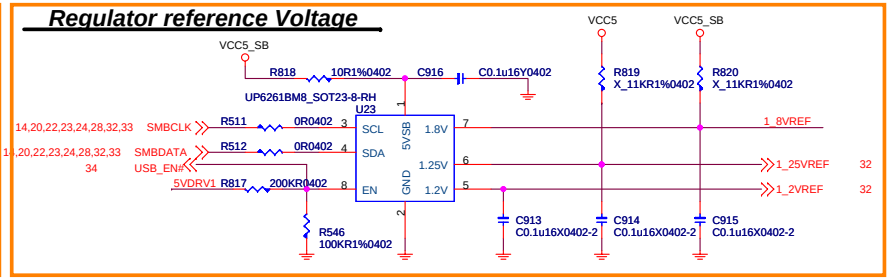
DDRII Power (1.8V , 20A)



DDR VTT Power (0.9V , 1A)



Regulator reference Voltage



MSI MICRO-START INT'L CO.,LTD.

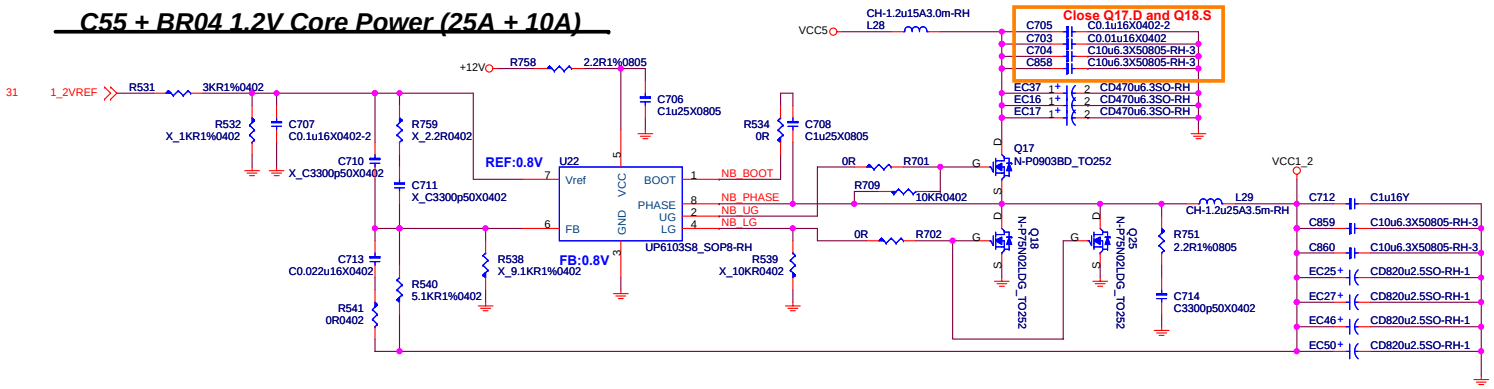
Title: MS12 ACPI Controller

Size	Document Number	Rev
	MS-7510	2.0

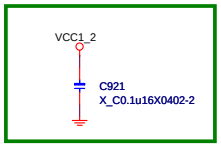
Date: Tuesday, January 15, 2008 Sheet 31 of 42

uPI Power Regulator

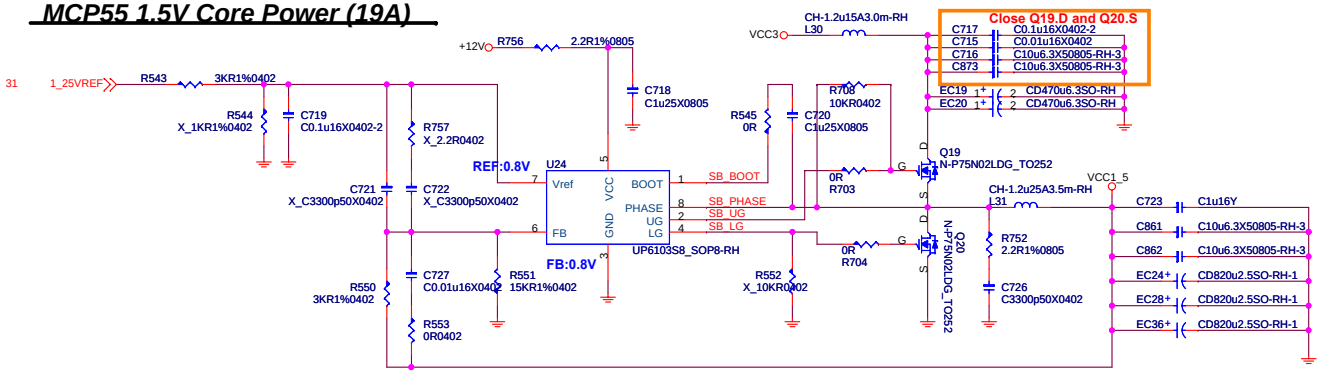
C55 + BR04 1.2V Core Power (25A + 10A)



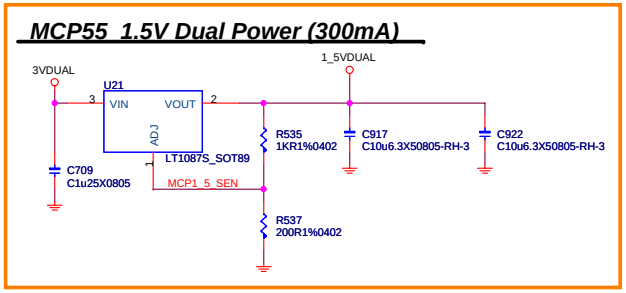
For EMI



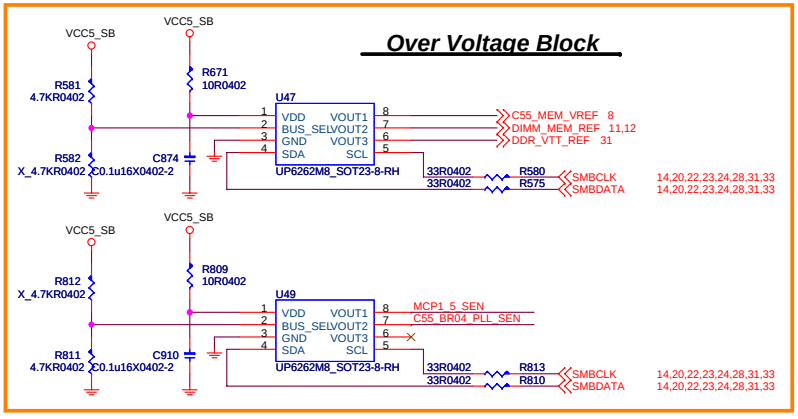
MCP55 1.5V Core Power (19A)



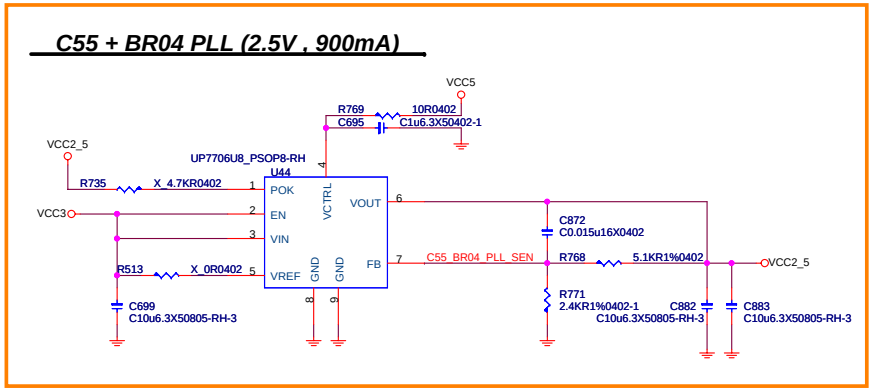
MCP55 1.5V Dual Power (300mA)

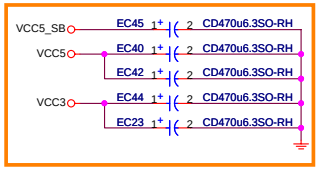


Over Voltage Block



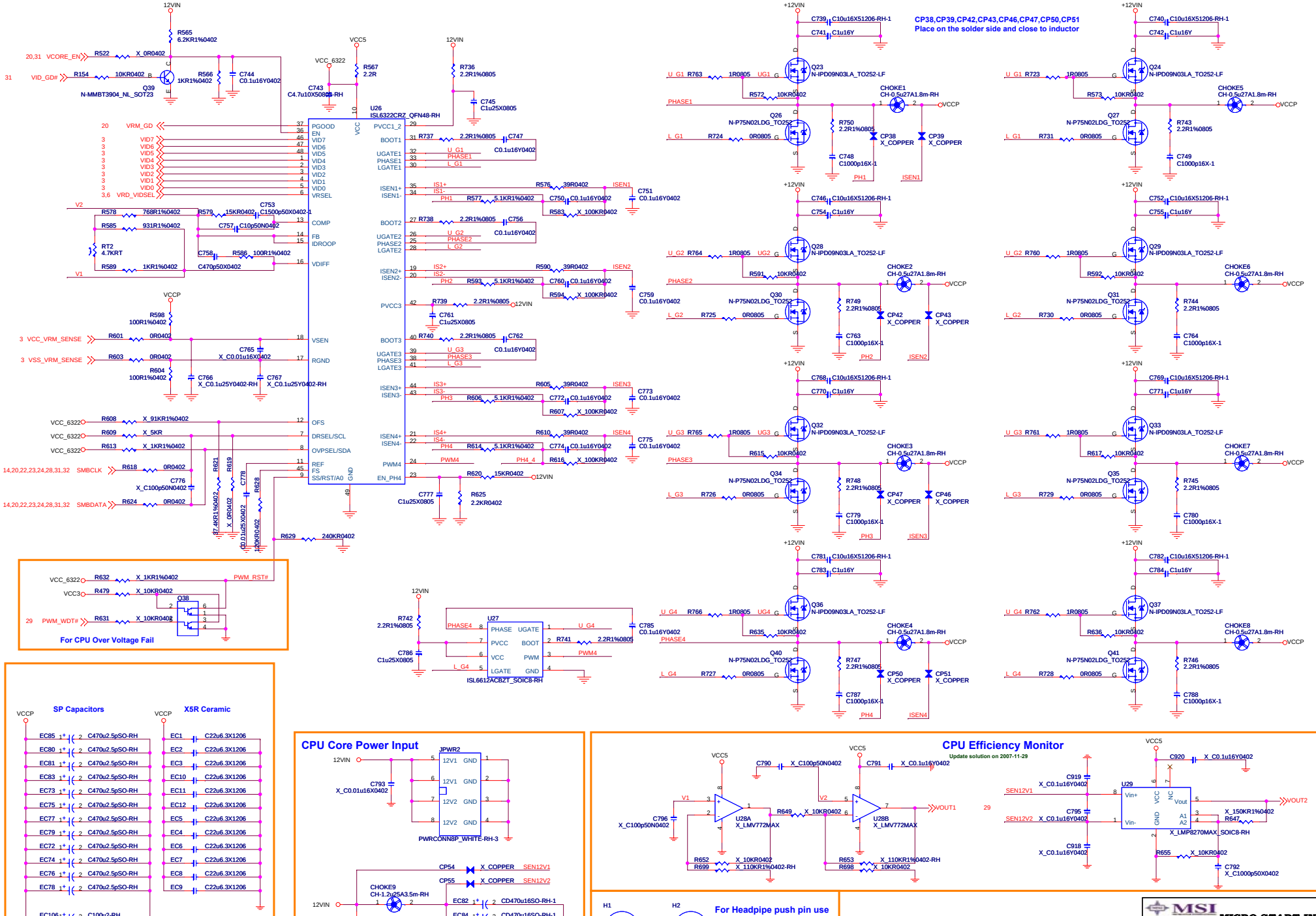
C55 + BR04 PLL (2.5V , 900mA)





MSI MICRO-START INT'L CO.,LTD.		
Title: uPI Power Regulator		
Size	Document Number: MS-7510	Rev: 2.0
Date: Tuesday, January 15, 2008	Sheet 32 of 42	

Voltage Regular Module (VRD11)



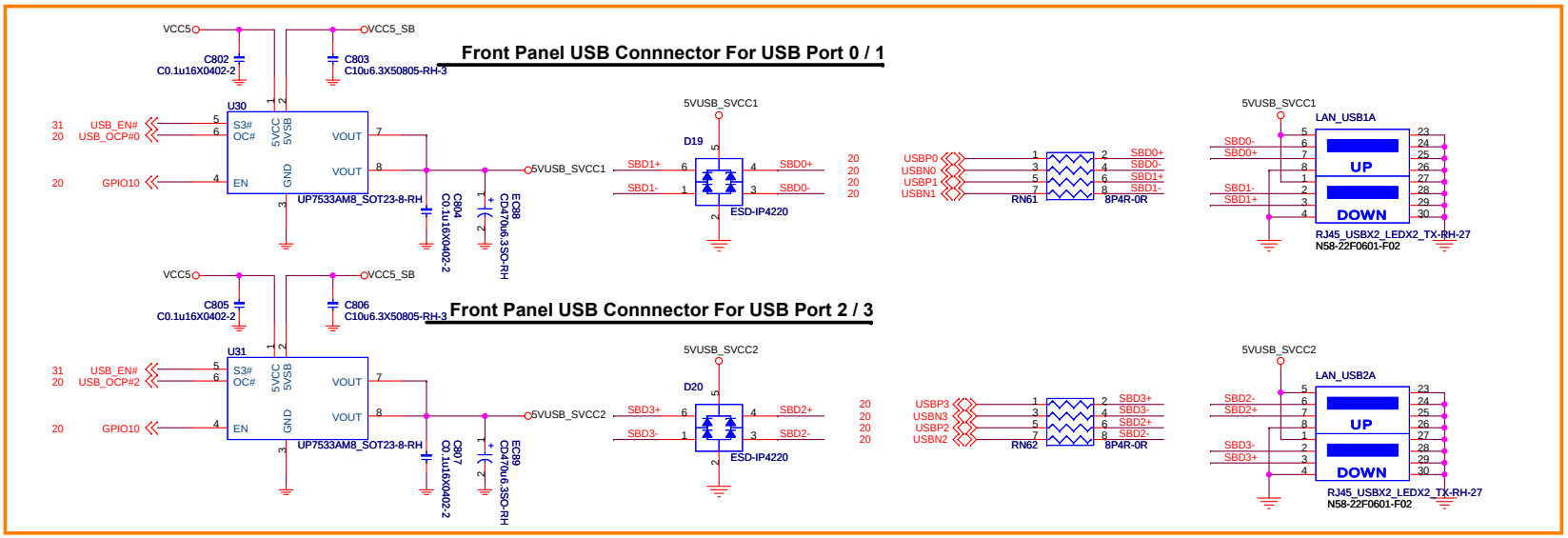
MSI MICRO-START INT'L CO.,LTD.

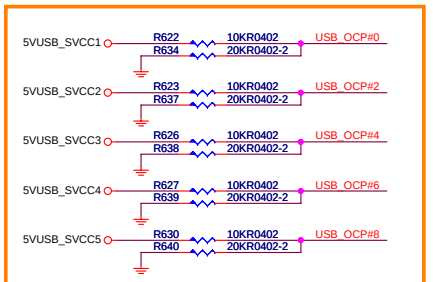
Title: VRD11- ISL6322 4 Phase 2 Channel

Size	Document Number	Rev
	MS-7510	2.0

Date: Tuesday, January 15, 2008 Sheet 33 of 42

Front Panel and Real I/O USB Connector



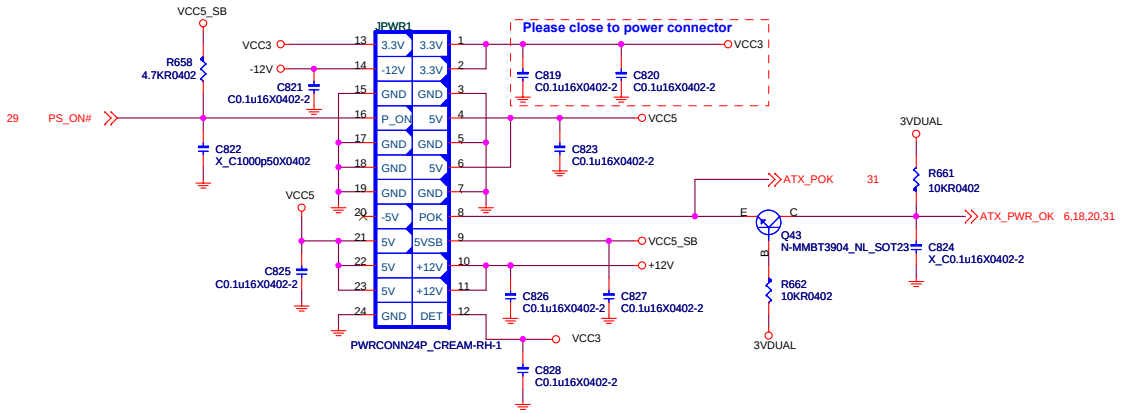


MSI Link to the Future MICRO-START INT'L CO.,LTD.

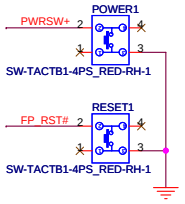
Title: Front / Rear USB Connectors		
Size	Document Number: MS-7510	Rev: 2.0
Date: Saturday, January 12, 2008	Sheet 34 of 42	

ATX Power Connector / Front Panel / LED

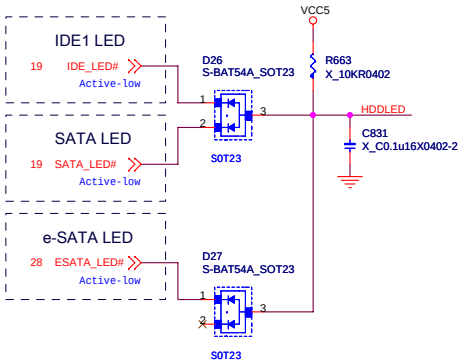
24 Pin ATX Power Connector



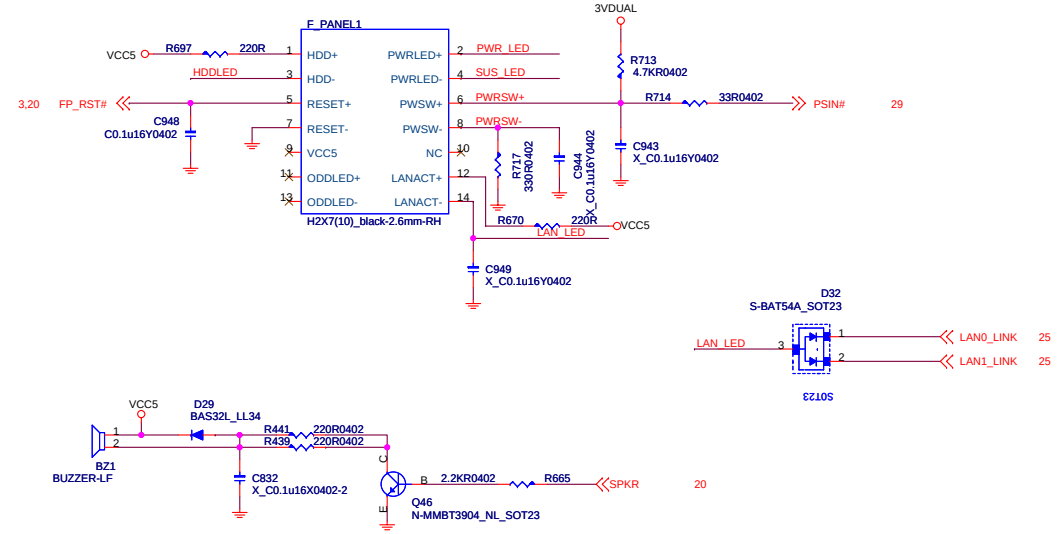
Power and Reset Button



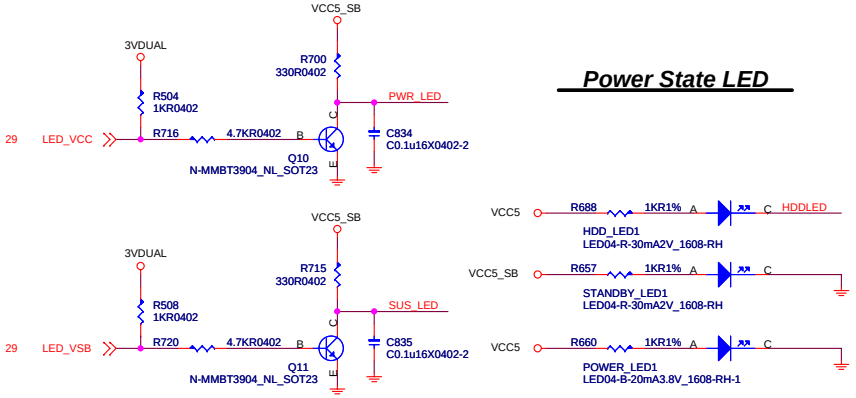
HDD LED



acer Front Panel Connector

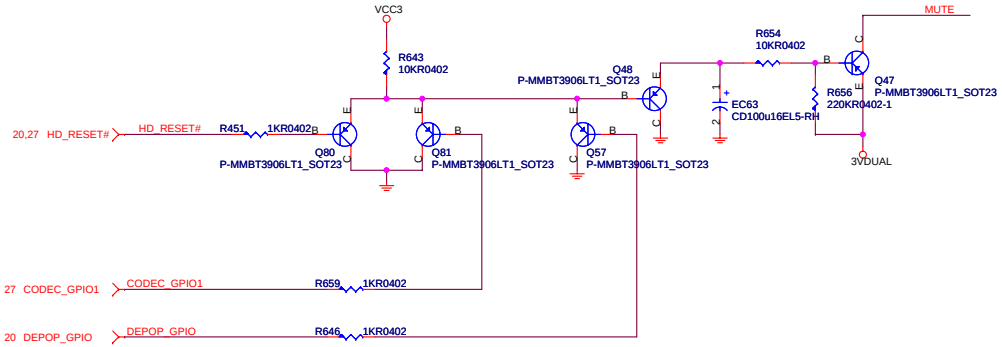


Power State LED

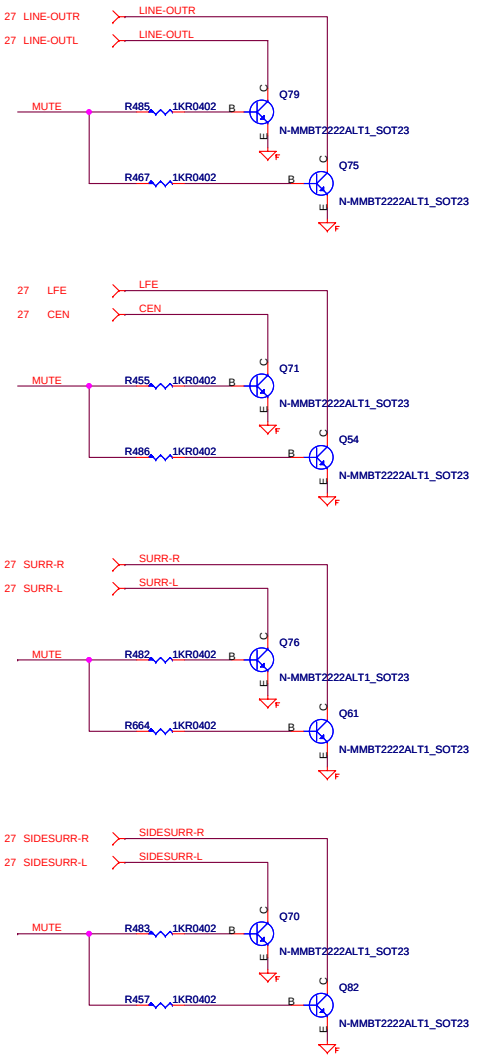


MICRO-START INT'L CO.,LTD.		
Title: ATX Connector / Front Panel / LED		
Size	Document Number: MS-7510	Rev: 2.0
Date: Wednesday, January 16, 2008	Sheet 35 of 42	

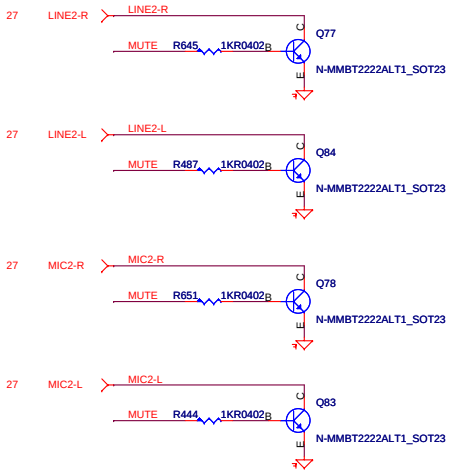
Audio De-Pop Control Circuit



Rear Audio Port De-Pop Circuit

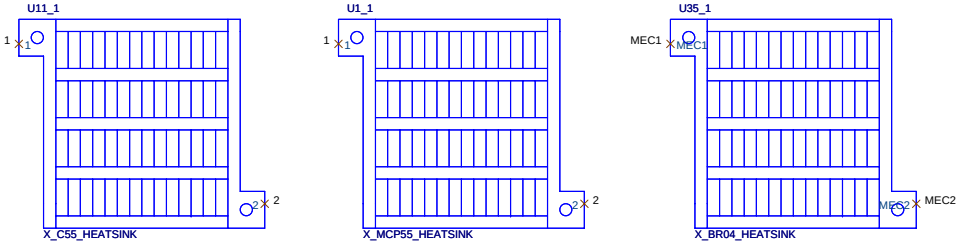


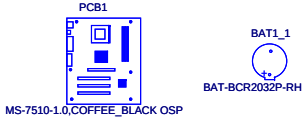
Front Audio Port De-Pop Circuit



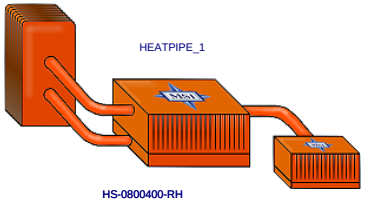
MICRO-START INT'L CO.,LTD.

Title: Audio De-Pop Circuit		
Size: Custom	Document Number: MS-7510	Rev: 2.0
Date: Saturday, January 12, 2008	Sheet 36 of 42	

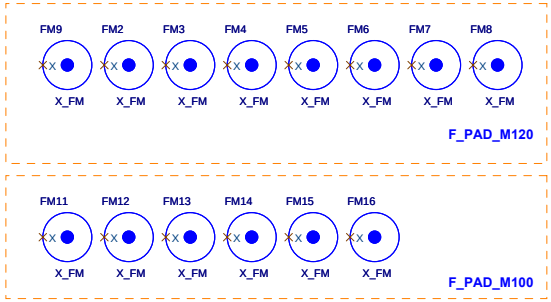




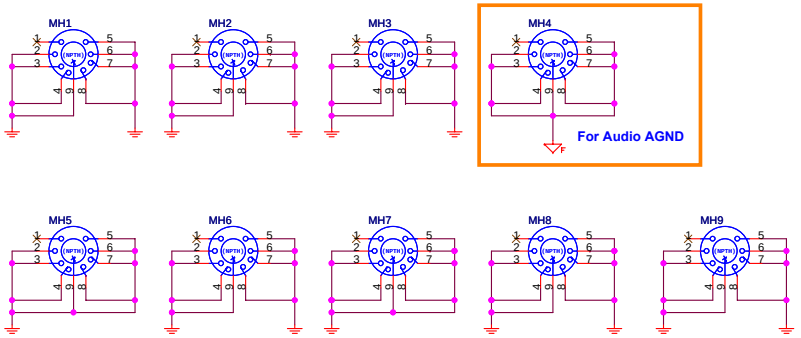
P30-0751020-G37 (精成)
P30-0751020-Y43 (宵富)



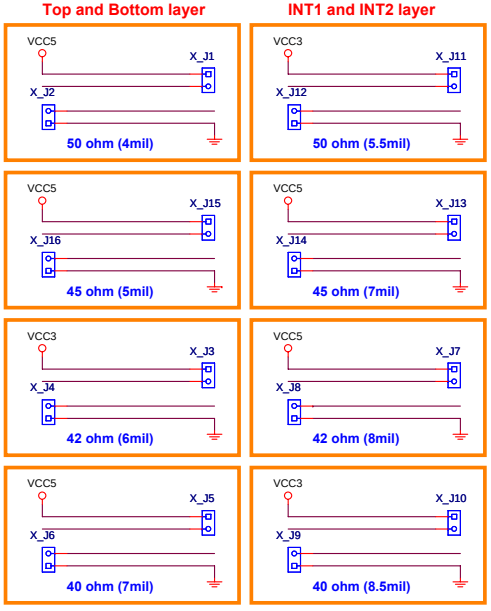
Optics Orientation Holes



PCB Mounting Holes



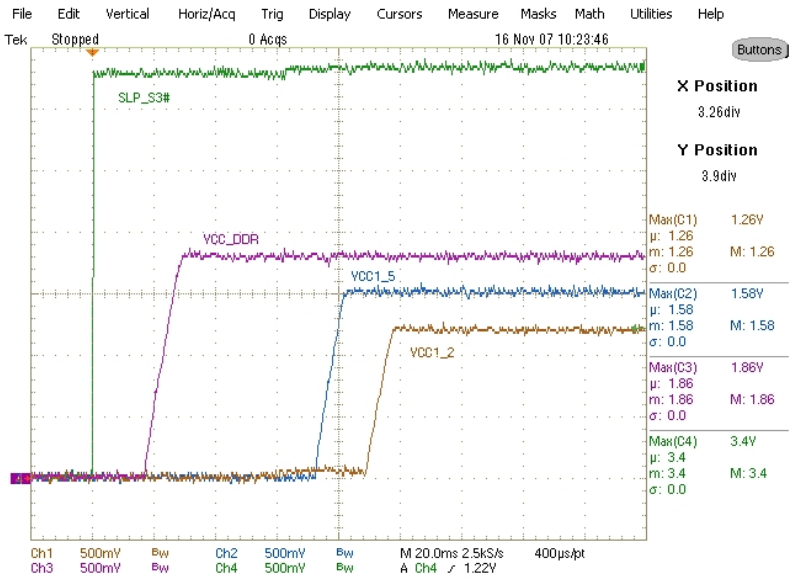
Simulation



MICRO-START INT'L CO.,LTD.

Title: Manual Parts		
Size	Document Number: MS-7510	Rev: 2.0
Date: Saturday, January 12, 2008	Sheet 37 of 42	

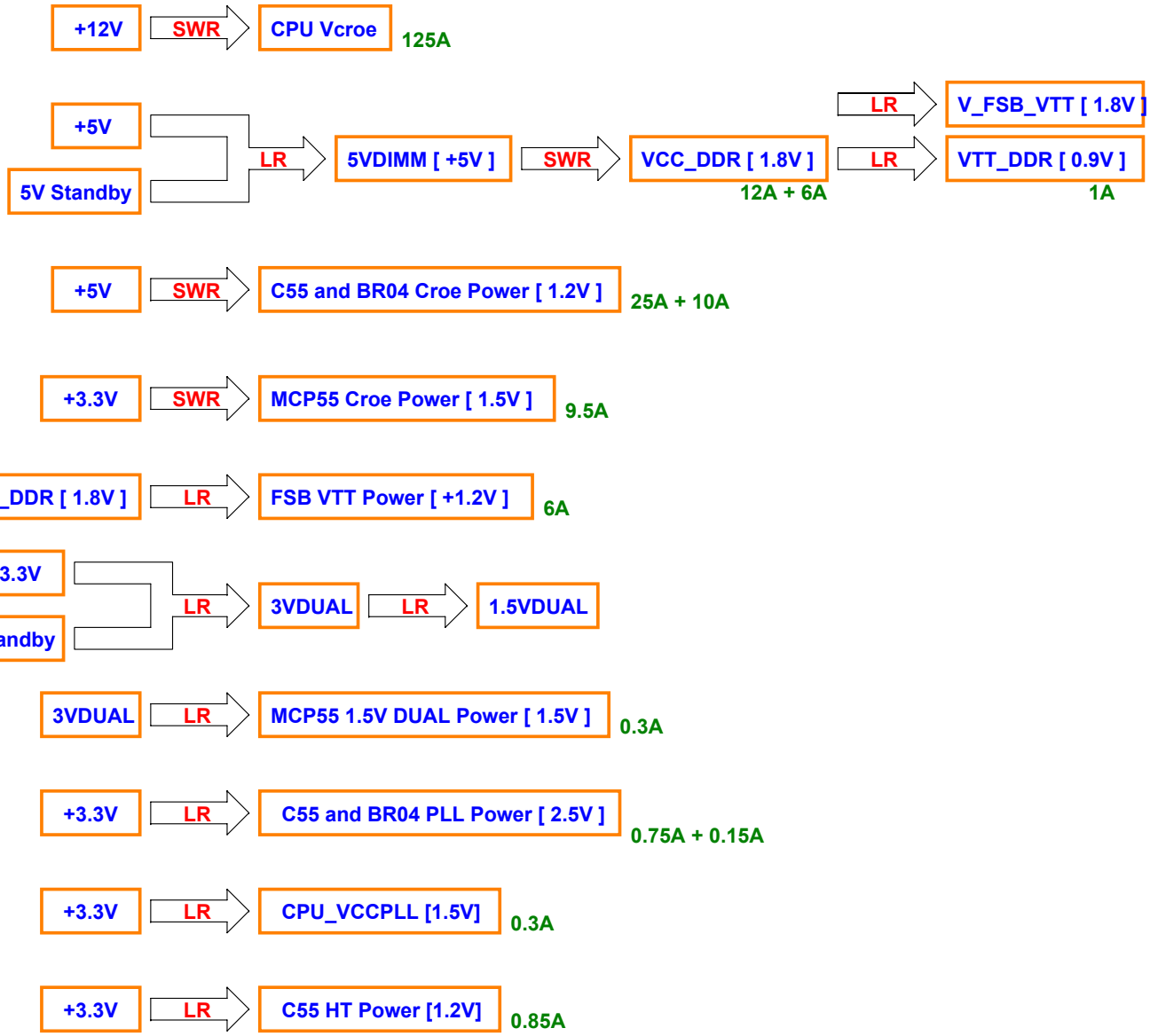
Power On/Off Sequence



MICRO-START INT'L CO.,LTD.

Title: Power On/Off Sequence		
Size	Document Number: MS-7510	Rev: 2.0
Date: Friday, January 04, 2008	Sheet 38 of 42	

Syatem Power Map



MSI Link to the Future MICRO-START INT'L CO.,LTD.

Title: Cover Sheet		
Size	Document Number: MS-7510	Rev: 2.0
Date: Friday, January 04, 2008	Sheet 39 of 42	

Configuration & GPIO

PCI Configuration

DEVICE	MCP1 INT Pin	REQ#/GNT#	IDSEL	CLOCK	PCI Reset
PCI slot 1	PIRQ#A	PCI0REQ# PCI0GNT#	AD21	PCI_CLK0	PCISLOT_RST#

DDRII DIMM Configuration

DIMM1	DIMM2	DIMM3	DIMM4
A0 1010000B	A4 1010010B	A2 1010001B	A6 1010011B
0A	0B	1A	1B

SMBus Distribution

SMBus	Power	Load
SMBDATA SMBCLK	VCC3	MCP55 , JM363 , PWM , Super I/O , uPI Power IC PCI Express x16 Slot * 3 , PCI Express x 8 Slot * 1 , PCI Express x 1 Slot * 1 , PCI Slot
SMB_MEM_DATA SMB_MEM_CLK	VCC3_SB	MCP55

System Reset Signal

Signal	Device
PE_RESET#	BR04
H_CPURST#	CPU
HTMCP_RST#	C55
PE_A_RESET#	BR04 PCI Express x 16 Primary Slot
PE_B_RESET#	BR04 PCI Express x 16 Secondary Slot
PEA_RESET#	JMicron JMB363 eSATA Controller JMicron JMB381 IEEE 1394a Host Controller
PEB_RESET#	MCP55 PCI Express x 16 Slot MCP55 PCI Express x 8 Slot MCP55 PCI Express x 1 Slot
PCISLOT_RST#	MCP55 PCI Slot
C55_PCIRST#	C55
SB_IDE_RST#	Master IDE Connector
SIO_RST#	Super I/O
PEX_RESET#	LAN1_MARVELL/88E8056
PEX_RESET#	LAN2_MARVELL/88E8056

SuperI/O GPIO Function

Pin Name	Function Description
GP4	CPU_GTL_REF Select
GP5	Reset PWM
SLOTOCC#	Detect CPU remove or not
COPEN#	Detect Case Open or not

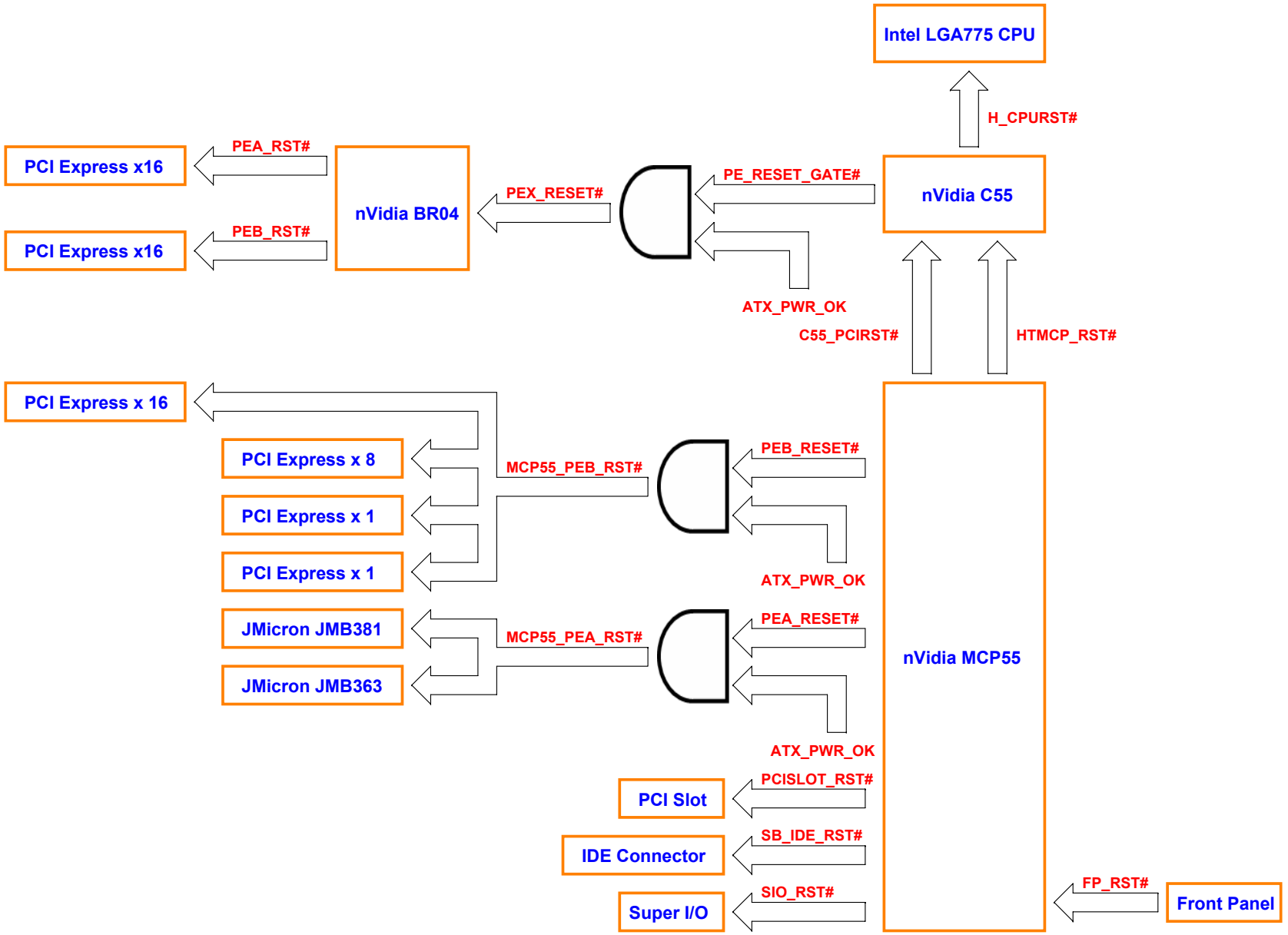
MCP55 GPIO Function

Pin Name	Function Description
GP10	USB Connector OC# Detect
GP6	LAN0_DISABLE#
GP7	LAN1_DISABLE#

Device Clock Signal

Signal	Device
CK_H_CPU# CK_H_CPU	C55 to CPU
HTMCP_DWNCLK0 HTMCP_DWNCLK0#	C55 to MCP55
HTMCP_UPCLK0 HTMCP_UPCLK0#	MCP55 to C55
PE_BR04_CLK PE_BR04_CLK#	C55 to BR04
BF_PE_CLK BF_PE_CLK#	C55 to Clock Buffer
PE_BR04_REFCLK PE_BR04_REFCLK#	Clock Buffer to BR04
PE_A_REFCLK PE_A_REFCLK#	Clock buffer to Express x 16 Primary Slot
PE_B_REFCLK PE_B_REFCLK#	Clock Buffer to Express x 16 Secondary Slot
MCPOUT_200MHZ MCPOUT_200MHZ#	MCP55 to C55
C55_25MHZ	MCP55 to C55
PE0SB_CLK PE0SB_CLK#	MCP55 to Express x 16 Slot
PE5SB_CLK PE5SB_CLK#	MCP55 to Express x 8 Slot
PE1SB_CLK PE1SB_CLK# PE2SB_CLK PE2SB_CLK#	MCP55 to PCI Express x 1 Slot
PE3SB_CLK PE3SB_CLK#	MCP55 to JMicron JMB363 eSATA Controller
PE4SB_CLK PE4SB_CLK#	MCP55 to JMicron JMB381 IEEE1394a Controller
PCI_CLK0	MCP55 to PCI Slot
SIO_PCLK	MCP55 to FinTek 71883FG Super I/O
LPC_PCLK	MCP55 to JTPM Pin Header
PE_100M_LAN0 PE_100M_LAN0#	MCP55 to LAN1
PE_100M_LAN1 PE_100M_LAN1#	CLOCK BUFFER to LAN2

Syatem Reset Map



MICRO-START INT'L CO.,LTD.

Title: Reset Map		
Size	Document Number: MS-7510	Rev: 2.0
Date: Friday, January 04, 2008	Sheet 41 of 42	

2007-12-17 Modify to Version 2.0 for acer Beetle

Update circuit

1. Change LAN connector to meet acer requirement -- page 25
2. Change Audio connector to meet acer requirement -- page 27
3. Add Audio De-pop circuit to meet acer requirement -- page 20,36
4. Remove FDD and TPM -- page 29
5. Add four system fan to meet acer requirement -- page 30
6. Change USB connector to header -- page 34
7. Modify the front panel header to meet acer requirement -- page 35
8. Add 2x5 GPIO pin header to meet acer requirement -- page 20
1. Change PCIEX8 to PCI slot -- page 17,18,23

2007-12-18

1.change PCI slot2 to PCIEX8(PCI_E6) -- page 17,18,23,24
2.remove three system fan to meet acer requirement -- page 30

2007-12-19

1.remove JCD1 to meet acer requirement -- page 27
2.remove IDE2 to meet acer requirement -- page 28
3.change R271&R274 to 10Kohm from 1Kohm -- page 25
4.change C684&C686 to 22p from 0.1u -- page 25
5.remove R358 -- page 27
6.remove R395&R401 and change R357&R359 to suffuse from reserve -- page 27
7.remove D10 and change pin21(U13) pin name -- page 27
8.add C660&C679&C811&C812 470p Cap. -- page 27
9.remove C616&C617&C621&C622 -- page 27
10.change JSP1 connect for acer requirement -- page 27
11.add R821&R822 -- page 27

2007-12-20

1.change L4 to 30L600mA from 10uH100mA -- page 7

2007-12-24

1.change FB12,FB13,FB14,FB15 footprint to 0402 from 0603 -- page 29
2.4.remove CP49 -- page 29

2007-12-26

1.change EC18 to 10uX5R from 4.7uX5R -- page 27
2.change C854,C855,C852,C853,C850,C851 to 4.7uX5R from 10uX5R -- page 27
3.change R382 to 324ohm from 300ohm -- page 27
4.remove D6&R376&R377 -- page 27
5.add R364&R365 -- page 27
6.move R362 location between EC96 and L14 -- page 27
7.move R361 location between EC97 and L15 -- page 27
8.move R371 location between EC98 and L16 -- page 27
9.move R370 location between EC99 and L17 -- page 27
10.move R380 location between C852 and R823 -- page 27
11.move R379 location between C853 and R824 -- page 27
12.move R383 location between EC100 and L18 -- page 27
13.move R384 location between EC101 and L19 -- page 27
14.move R398 location between C850 and R821 -- page 27
15.move R397 location between C851 and R822 -- page 27

2007-12-28

01.Add serial resistor R666 (10k ohm) for Q53. – Page. 31
02.Don't stuff R48 (0 ohm) for Yorkfield CPU. – Page. 4
03.Change part number of Q65 to D03-2005K09-D07. – Page. 4
04.Change R302 & R303 from 22 ohm to 0 ohm. – Page. 25
05.Change C529, C531, C530 & C532 from 27pF to 22pF. – Page. 25
06.Change R191 & R201 from 22 ohm to 0 ohm. – Page. 19
07.Don't stuff Q7 & Q8. – Page. 29
08.Don't stuff R477 (For 7510-1.0 BOM select).
09.Change footprint of RN50 & RN51 from 0402 to 0603. – Page.26
10.Reserve C830 & C833 (0.1uF/0402) for VCC5 to GND. – Page.28, 29
11.Reserve C921 (0.1uF/0402) for VCC1_2 to GND. – Page.32

2007-12-31

01.Add Q85,R242,R825 and R826 for front panel LED controller circuit. - page. 20

2008-01-02

1.change LAN chip to MARVELL/88E8056(B1) from Realtek RTL8211BL-GR – Page. 25
2.add R900~R913 for remove RGMII interface – Page. 19

2008-01-03

1.change HEATPIPE P/N

2008-01-04

change super I/O to ITE IT8718F and circuit from FinTek F71882FG – Page. 29
remove parallel port did not pull up - Page29
R509 change to 10K ohm and non-stuff R514. - Page29
Remove R734 and short it directly - Page29
Remove C678,C677,C669 and C668. - Page29
Remove R491 - Page29
R452, R460 and R480 non-stuff - Page29
Modify VIN pin define(pin 13 for LED_VCC,pin 19 for PWM_WDT#) - Page29
add pin 79 for LED_VSB use - Page29
DTRA should change to pull low - Page29
Remove SKTOCC#. - Page3
Remove R236 and connect it to LPC_SMI#. - Page20
Remove R228 and RGMII_RST# - Page20
Remove DUAL_CTRL and R692 - Page31
Remove THRM#. - Page20
remove R366 and R667 - Page25
change U36 and U46 pin 47 conncet to VCC3 - Page25
change R367 and R440 to 0ohm(resever) - Page25

2008-01-04

remove R366 and R667 - Page25
change U36 and U46 pin 47 conncet to VCC3 - Page25
change R367 and R440 to 0ohm(resever) - Page25

2008-01-04

remove U45B and U50 - Page30
remove R501,R502,C688,C928,R526,R527,C689,R239 of sysfan1 controller - Page30
ADD U19 R169,R191,R549,C545,R546,C547,C548,R314,R311 for sysfan1 controller - Page30
remove R505,R503,C692,C931,R528,R529,C867,R240 of sysfan2 controller - Page30
ADD U20 R198,R200,C555,C551,C552,C553,C554,R312,R315 for sysfan2 controller - Page30
CHANGE Q73 Q74 to N-P3057_TO252 from P-P06P03LCG_SOT89 - Page30

2008-01-07

change the R497 from 1% to 5% resistor - Page30
Change the C880 to EC Cap(EC109_100uF) - Page30
remove U20,C551,C553,C552,and system fan2 use U19 controller - Page30

2008-01-08

Add C995&C998 for 3VDUAL - Page25
LAN chip pin40 and pin44 connect to 3VDUAL direct - Page25
remove C993&C951 - Page25
remove EC15 - Page25
change EC100&EC101 to 100u from 10u - Page27
add R450,R453,R454,R456,R458 pull high resister - Page29
move C950,C601,C622 location,between FB18 and C617 - Page25
move C616,C947,C621 location,between FB19 and C602 - Page25
move C991,C661,C941 location,between FB20 and C686 - Page25
move C684,C978,C940 location,between FB21 and C685 - Page25
change R504,R508 to 1Kohm5% from 1Kohm1% resistor - Page35

2008-01-09

swap RN38,RN58 pin connect for layout space - Page29
remove AVDDL0,AVDDL1,VDD0,VDD1 net name - Page25

2008-01-10

ADD CP56,CP57,CP58 for EMI request
REMOVE C254,C830 for EMI request

2008-01-11

ADD Q88,Q89,Q90,R310,R827,R316,R313,R829,R828,R830 -- Page20
CHANGE JGPIO PIN CONNECT PIN2_NON USE,PIN4_GPIO14,PIN7_WHITE_LED,PIN8_BLUE_LED -- Page20

MSI Link to the Future MICRO-START INT'L CO.,LTD.

Title: History		
Size	Document Number: MS-7510	Rev: 2.0
Date: Saturday, January 12, 2008	Sheet 42 of 42	